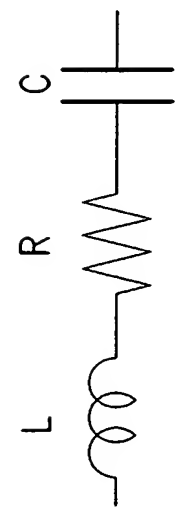
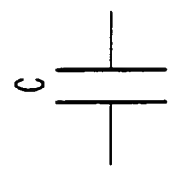


FIG. 1

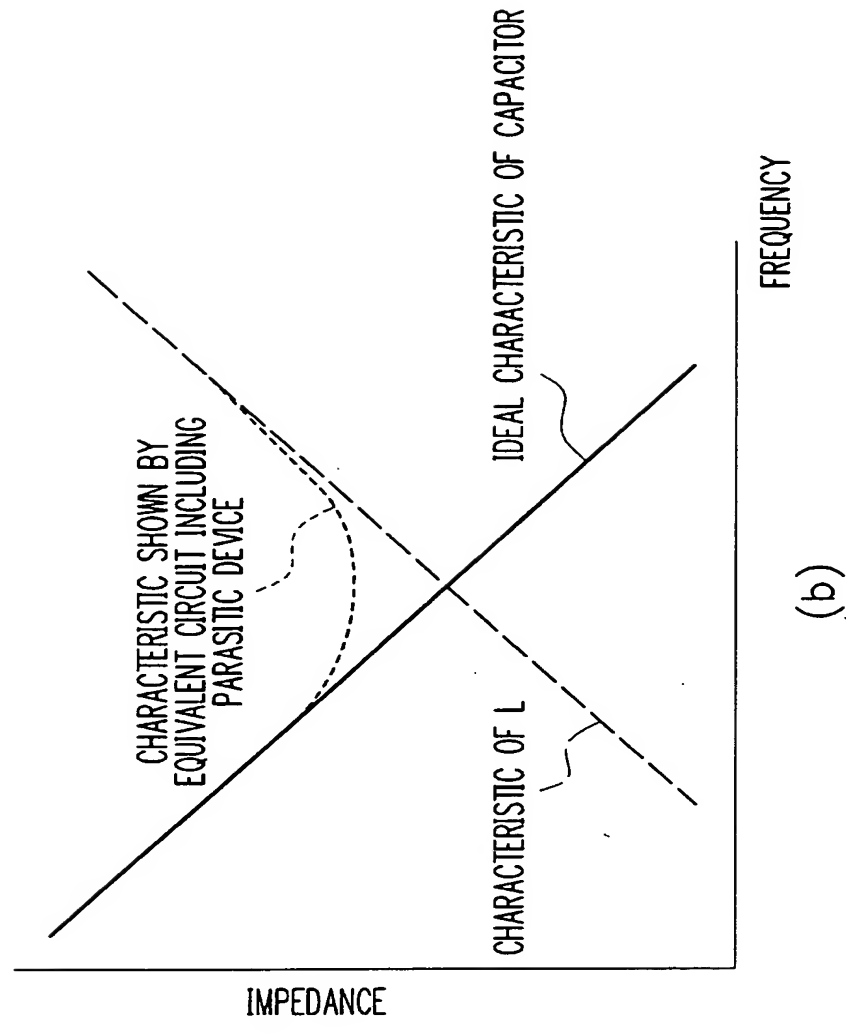


ACTUAL CAPACITOR
(EQUIVALENT CIRCUIT INCLUDING PARASITIC DEVICE)



IDEAL CHARACTERISTIC OF CAPACITOR

(a)



(b)

F I G. 2

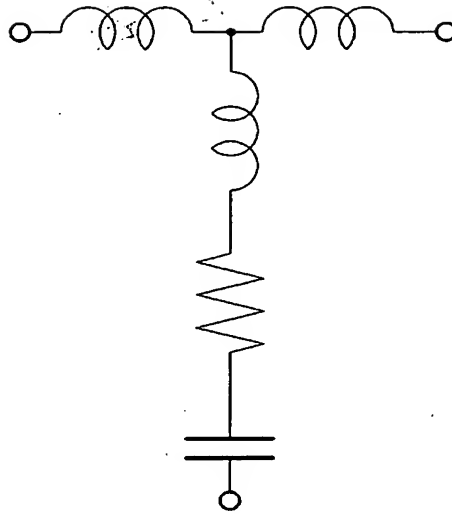


FIG. 3

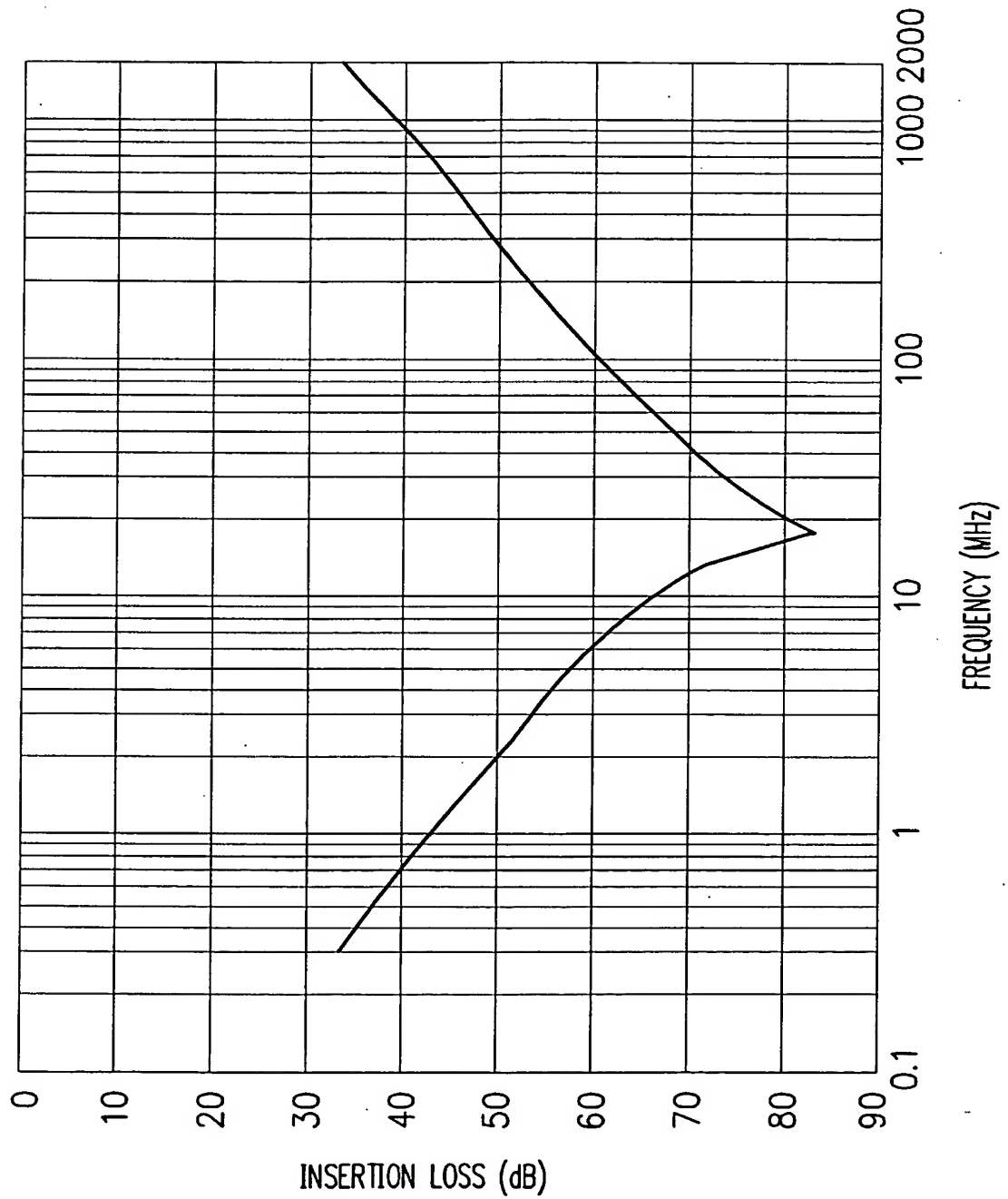
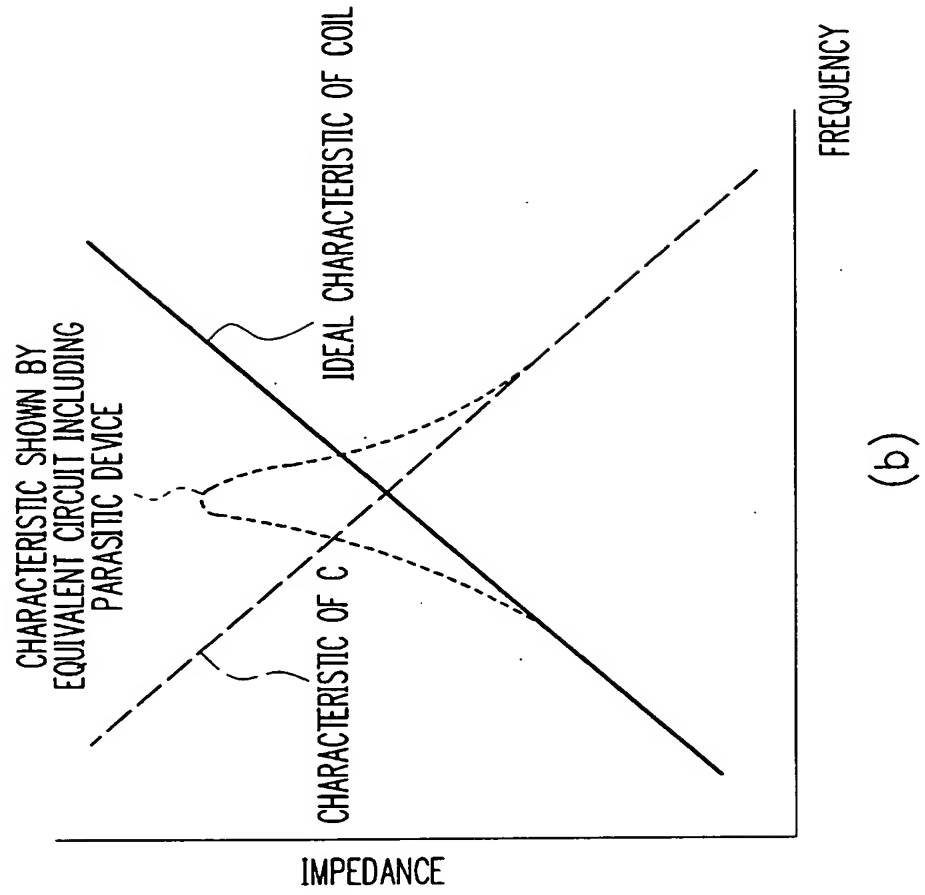
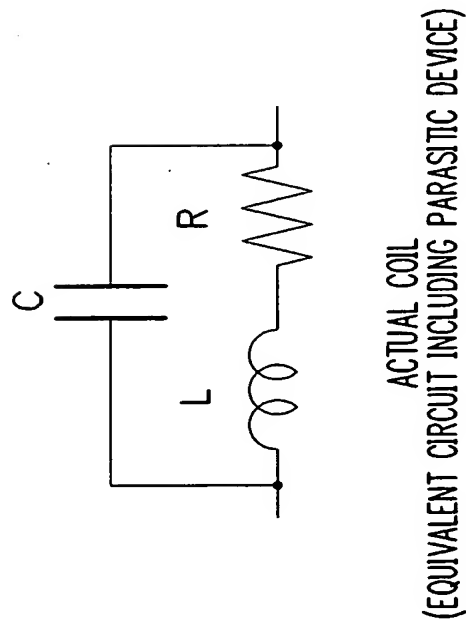


FIG. 4



(a)

(b)

FIG. 5

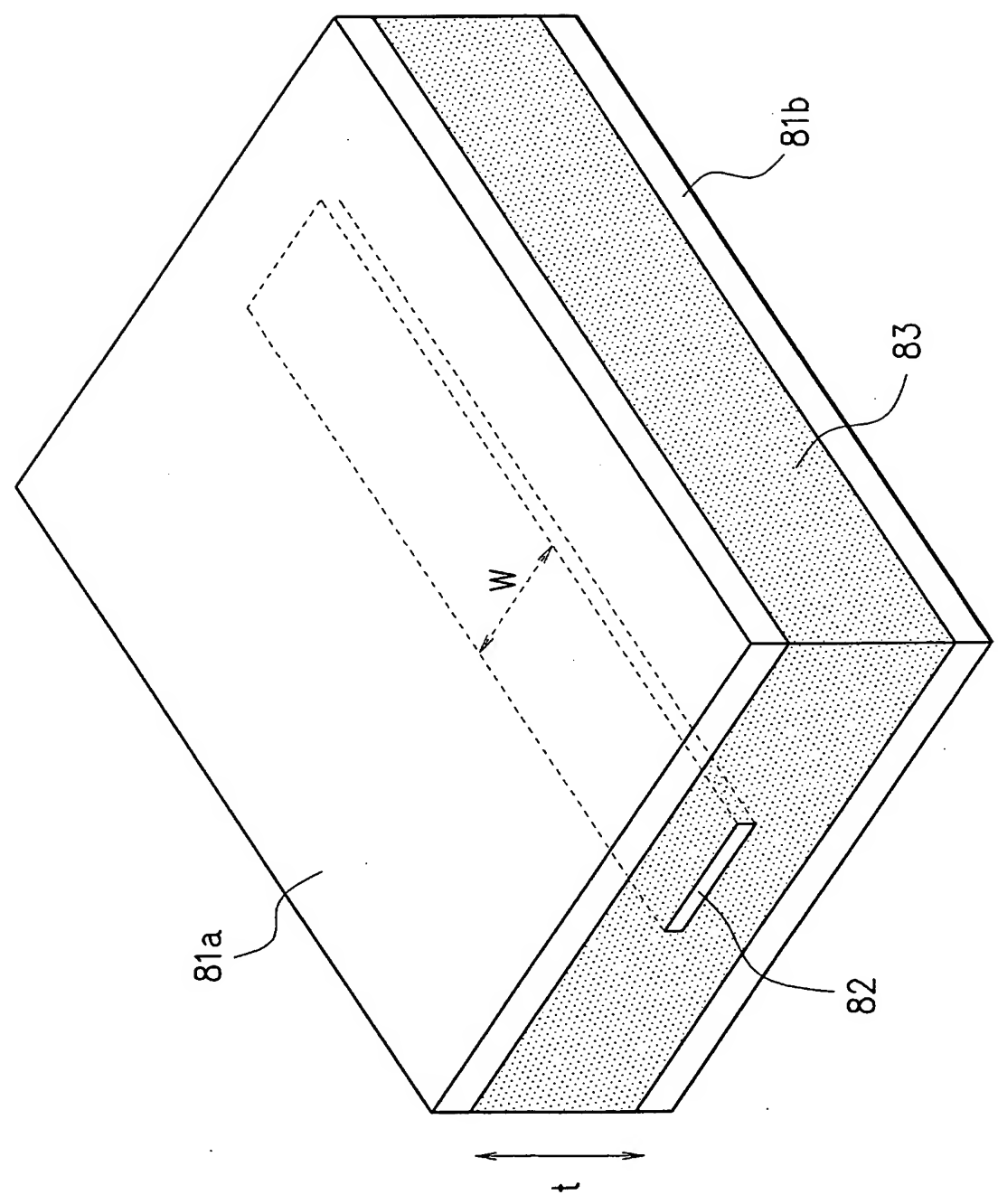


FIG. 6

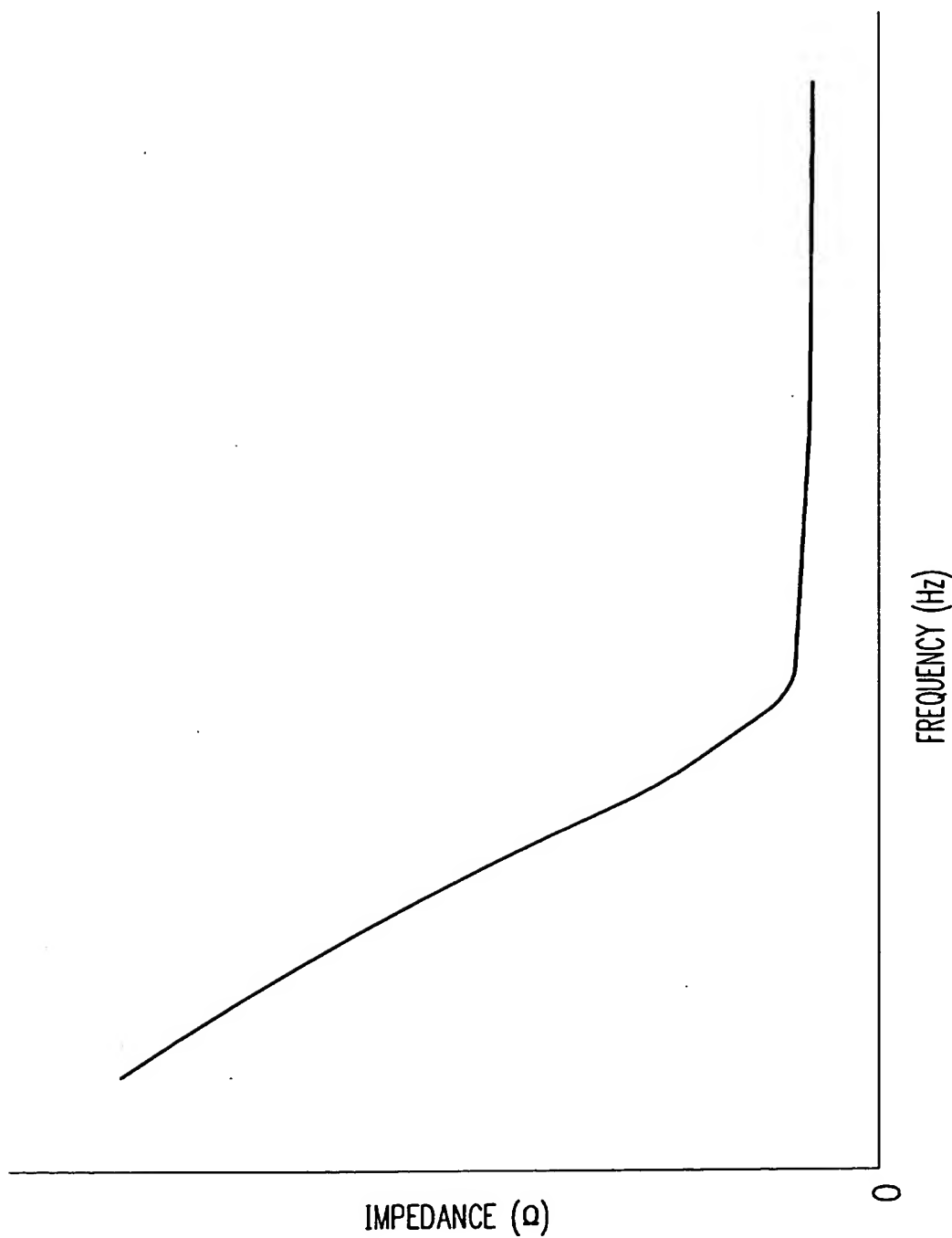
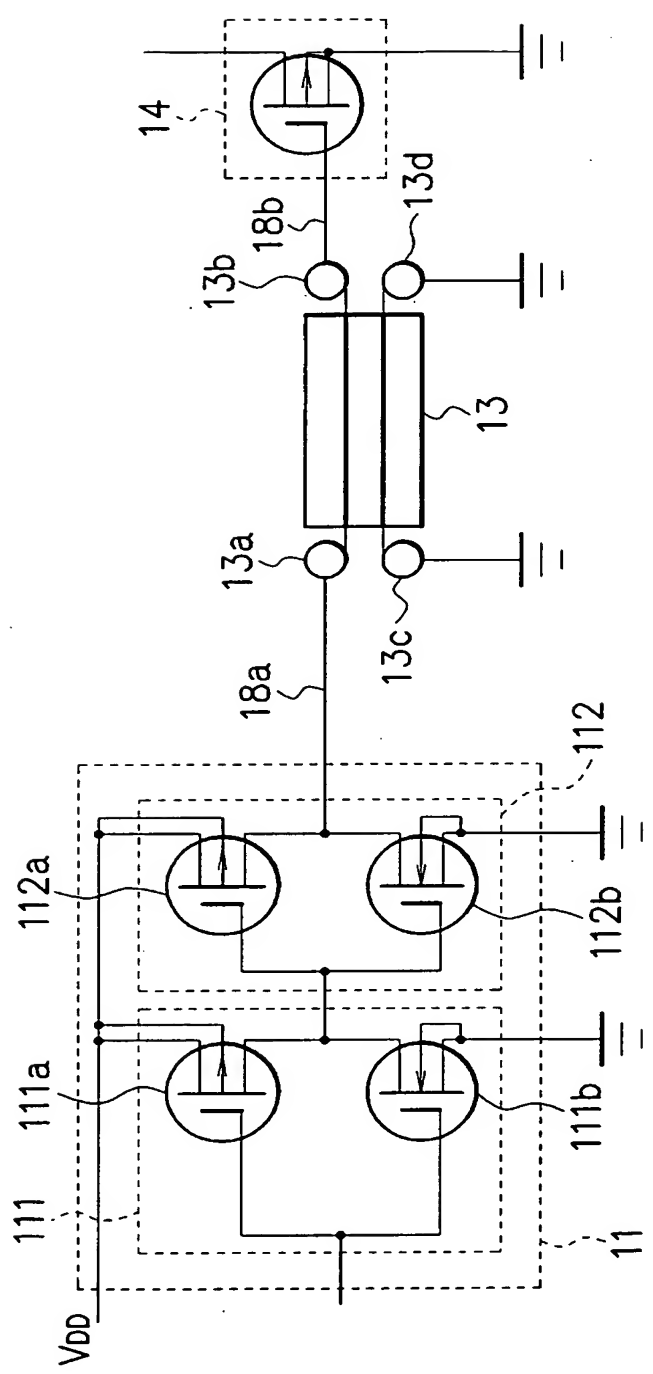
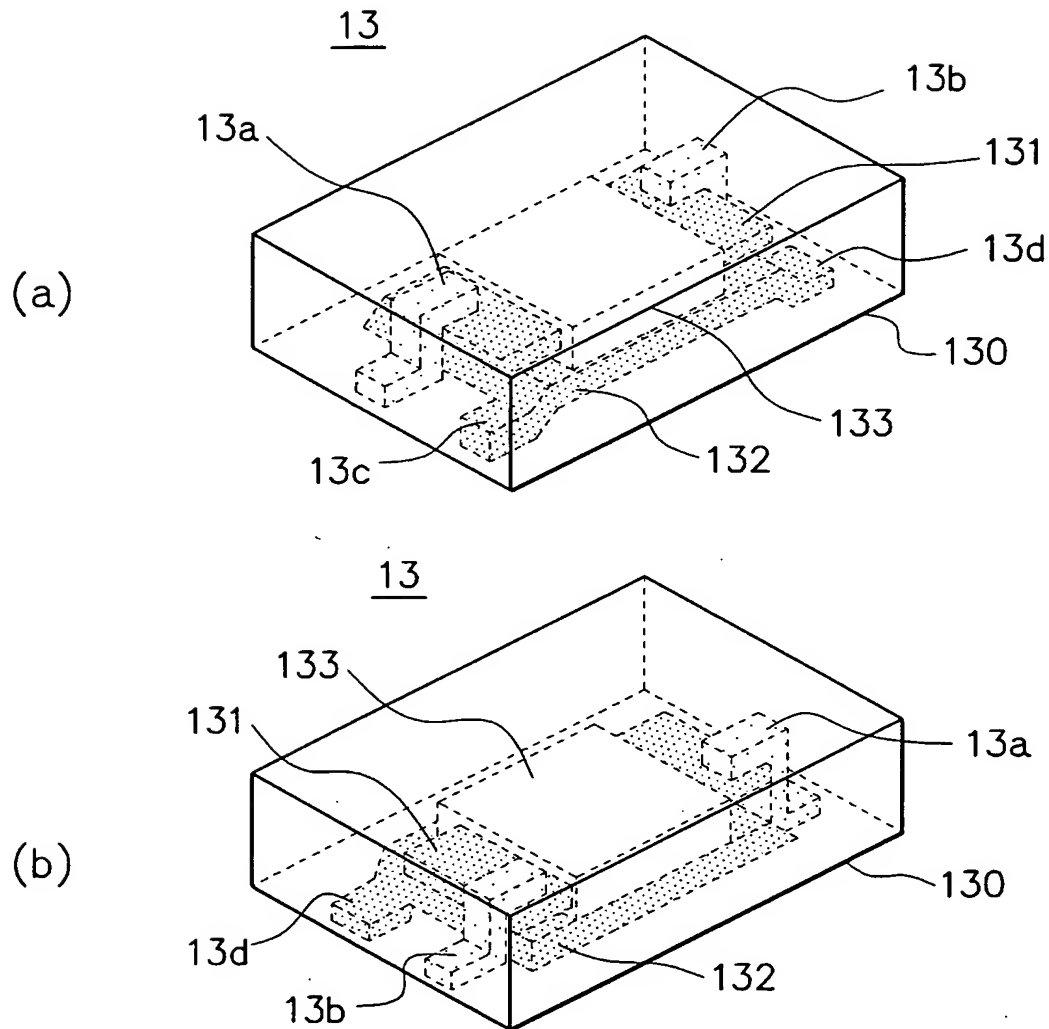


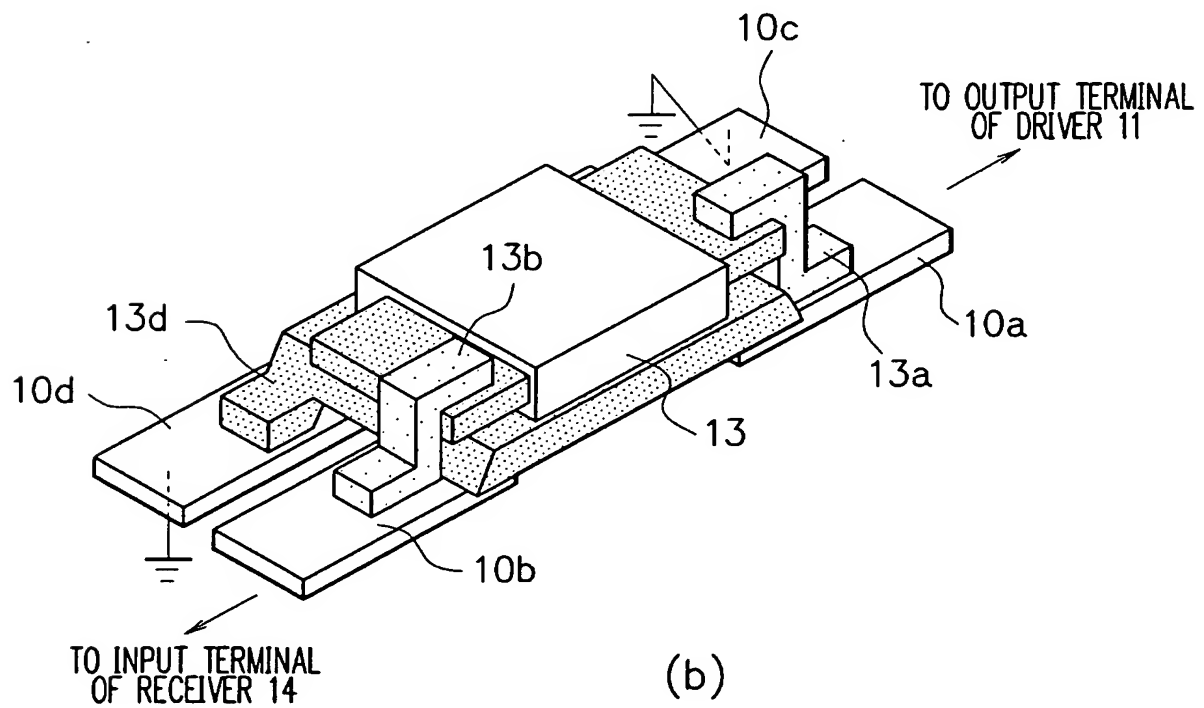
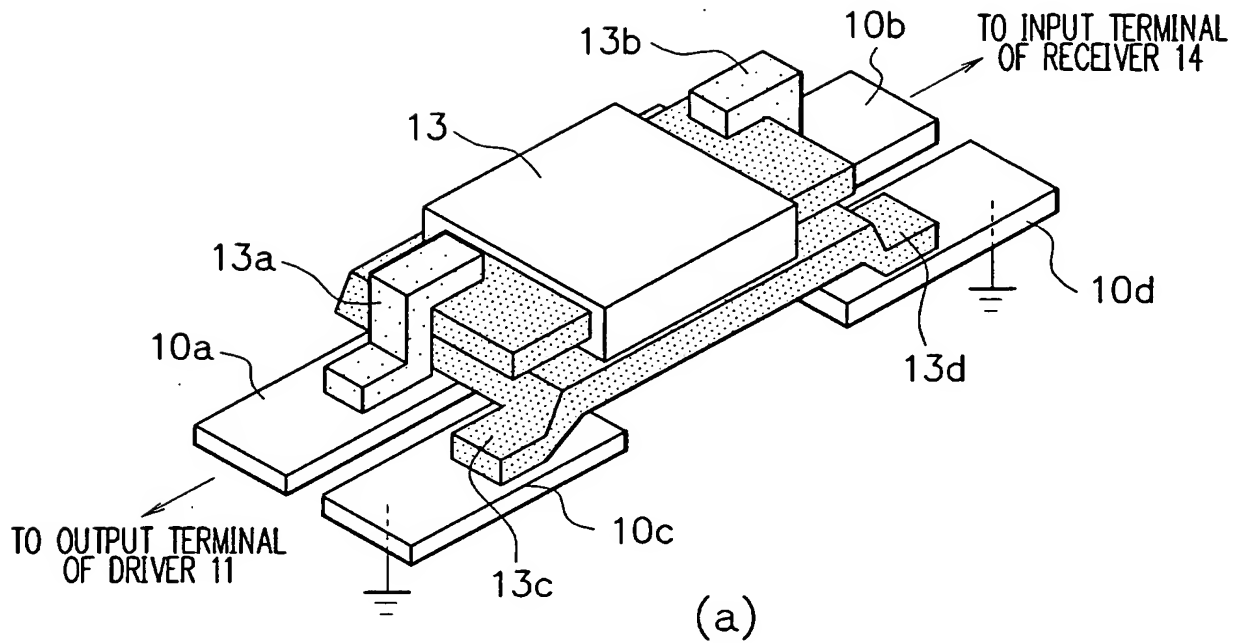
FIG. 7



F I G. 8



F I G. 9



F I G. 10

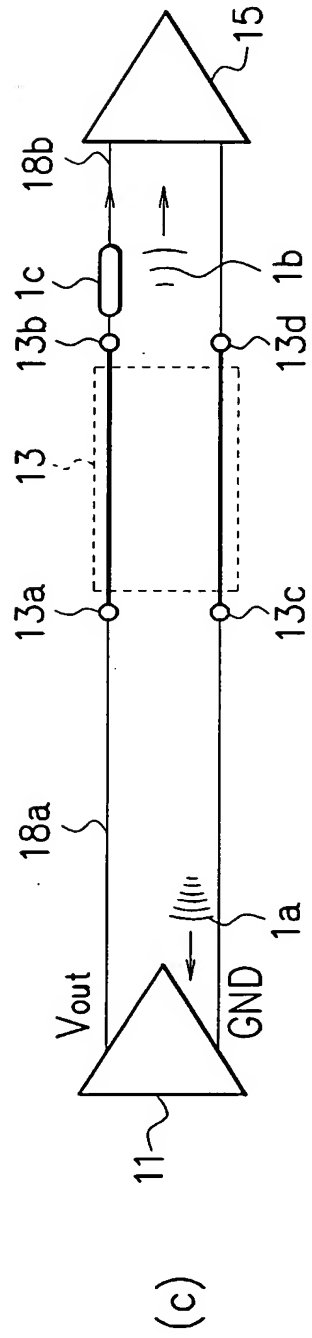
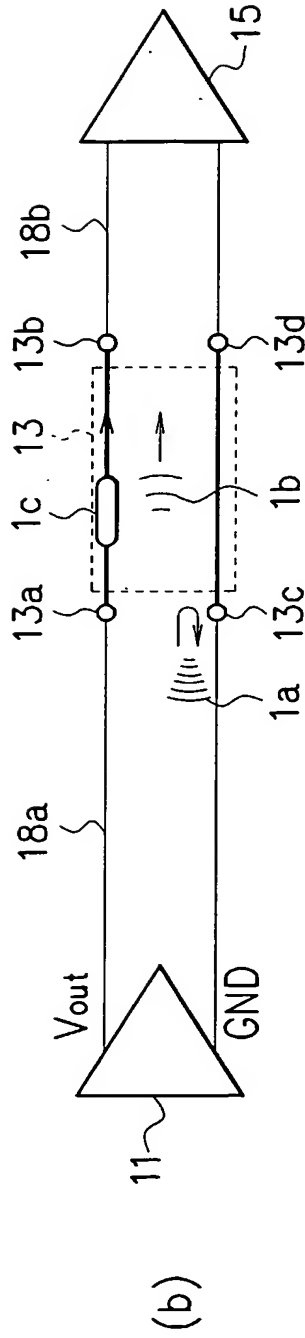
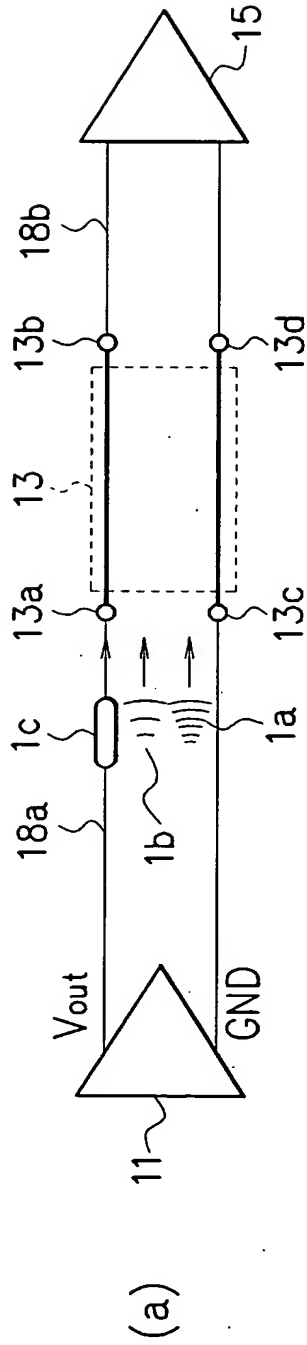


FIG. 11

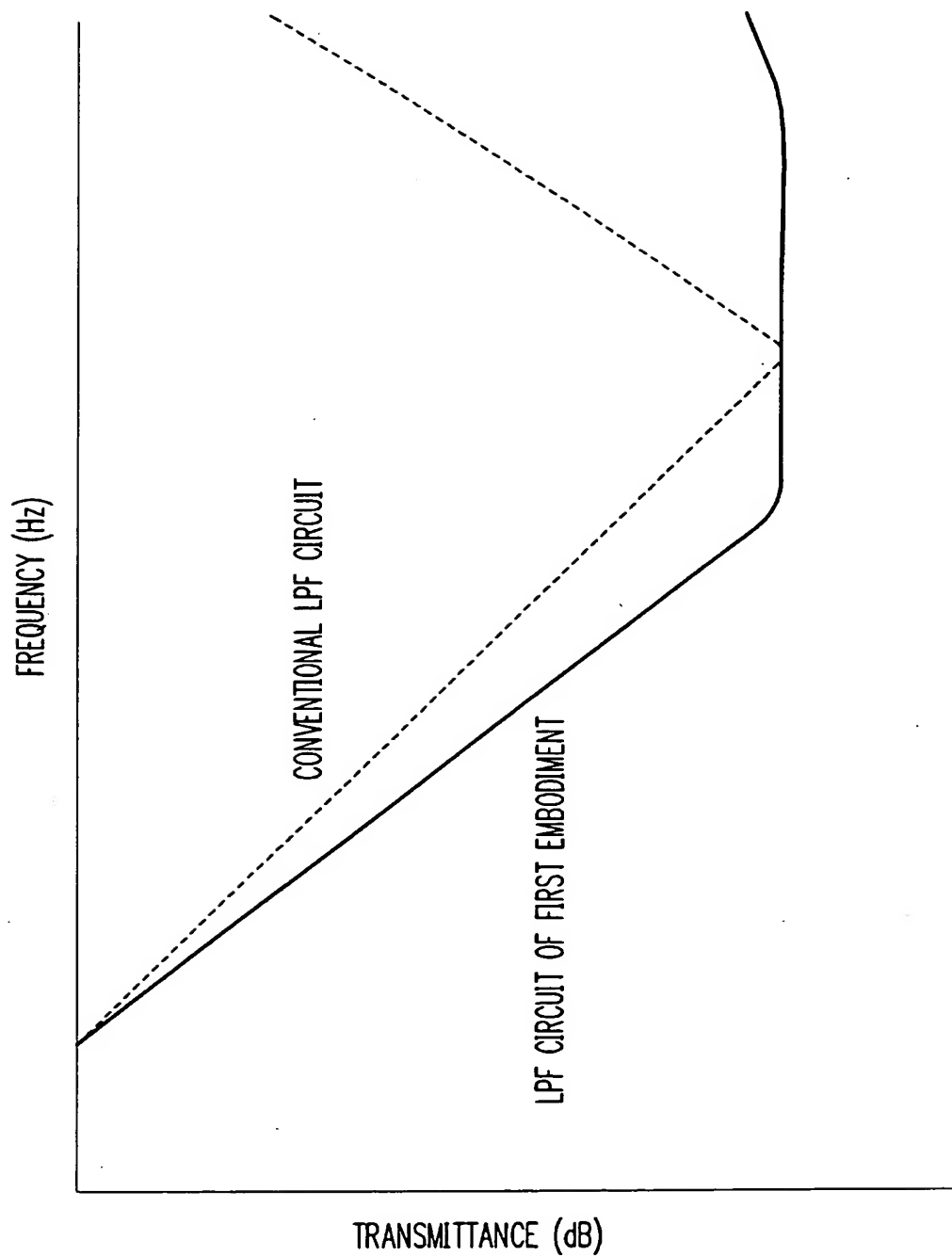


FIG. 12

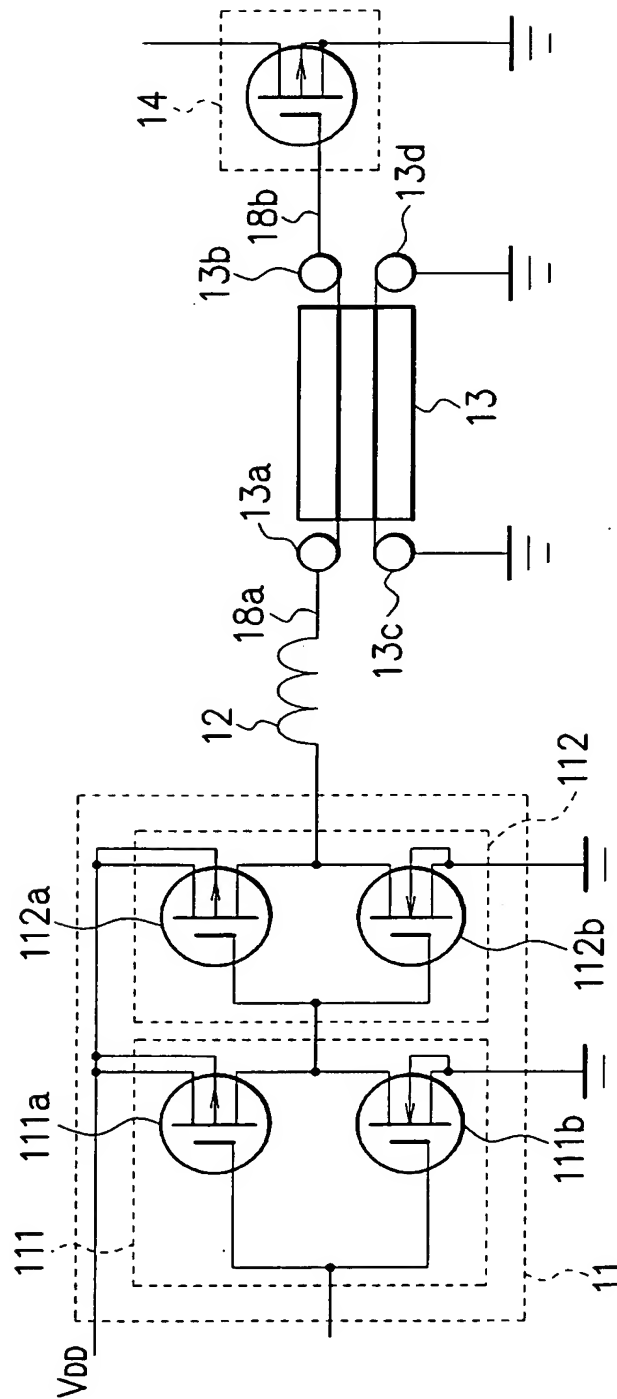


FIG. 13

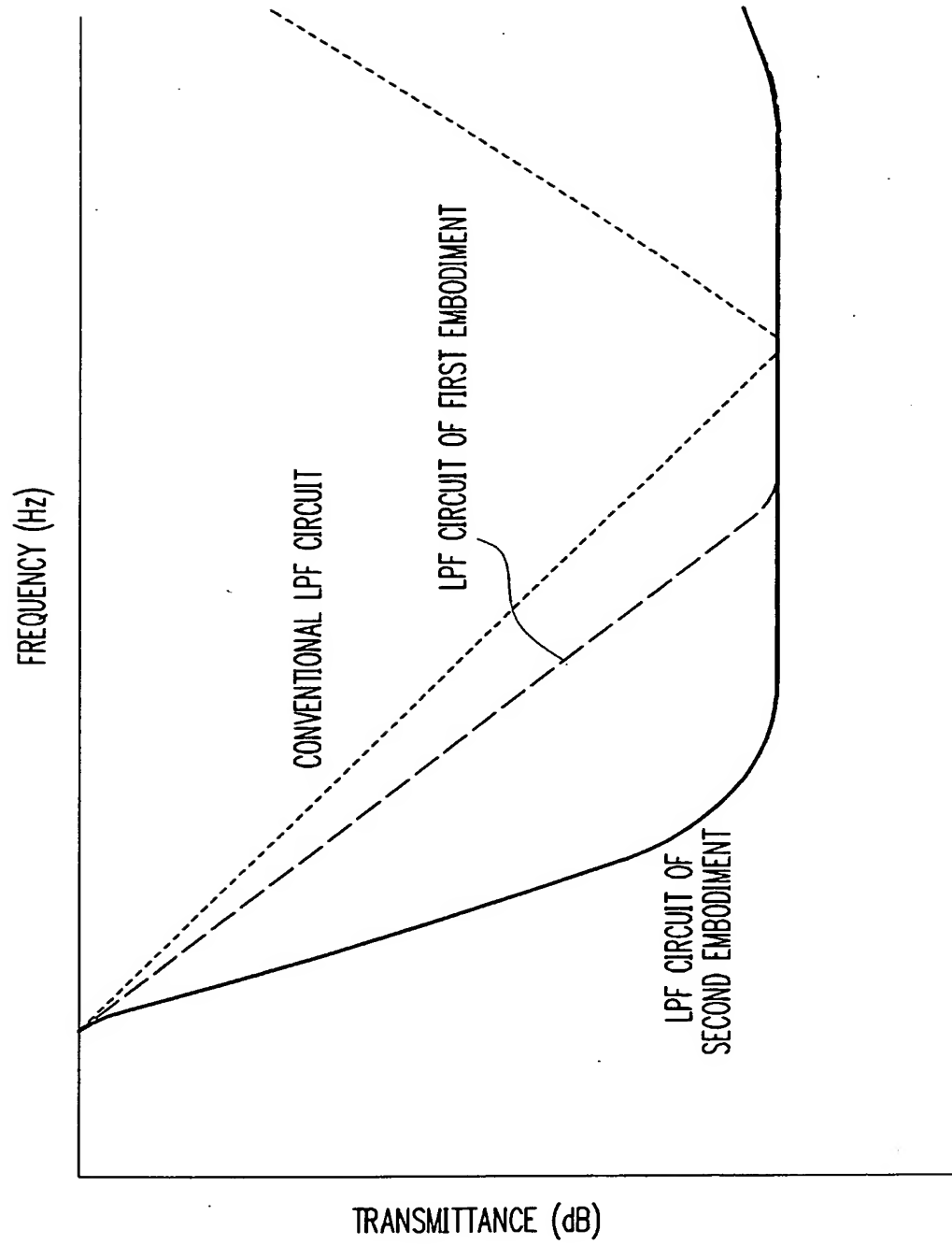


FIG. 14

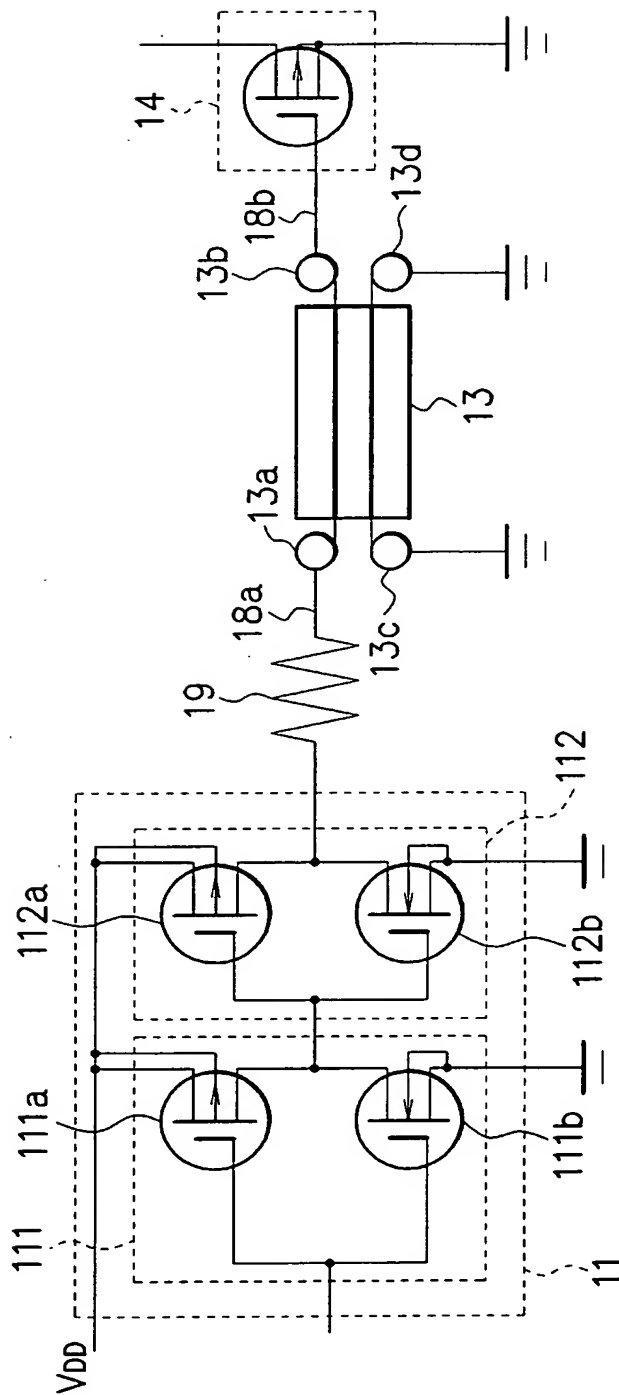
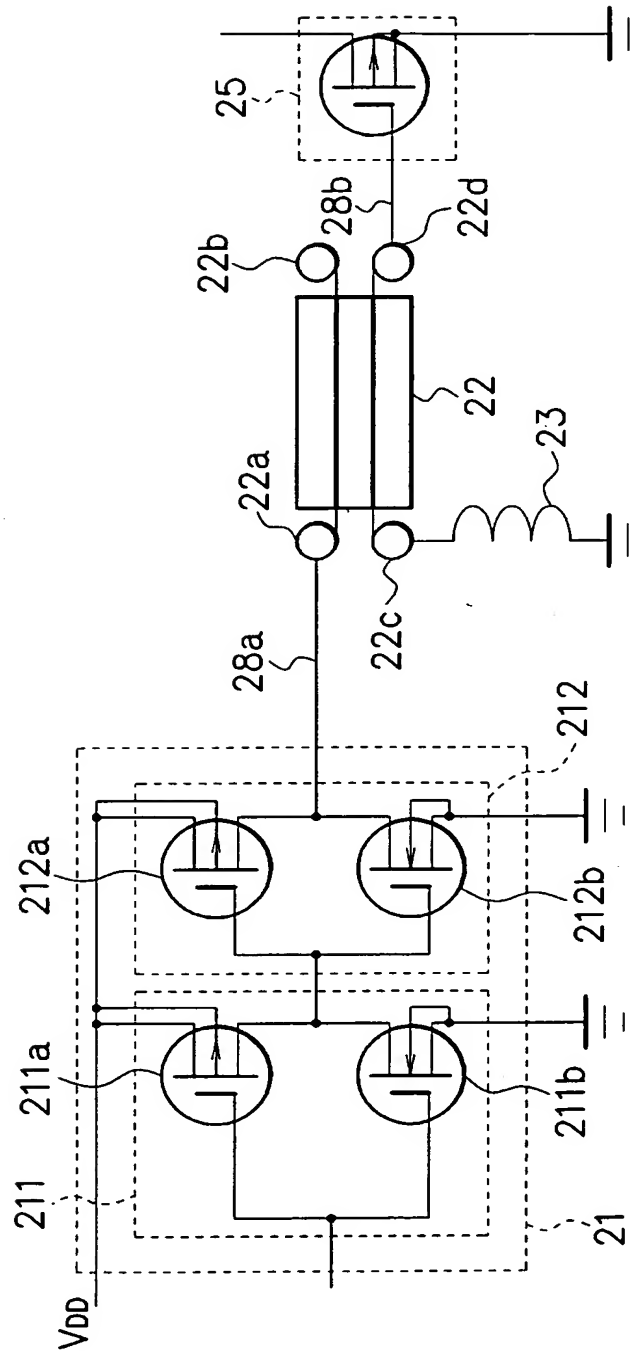


FIG. 15



F I G. 16

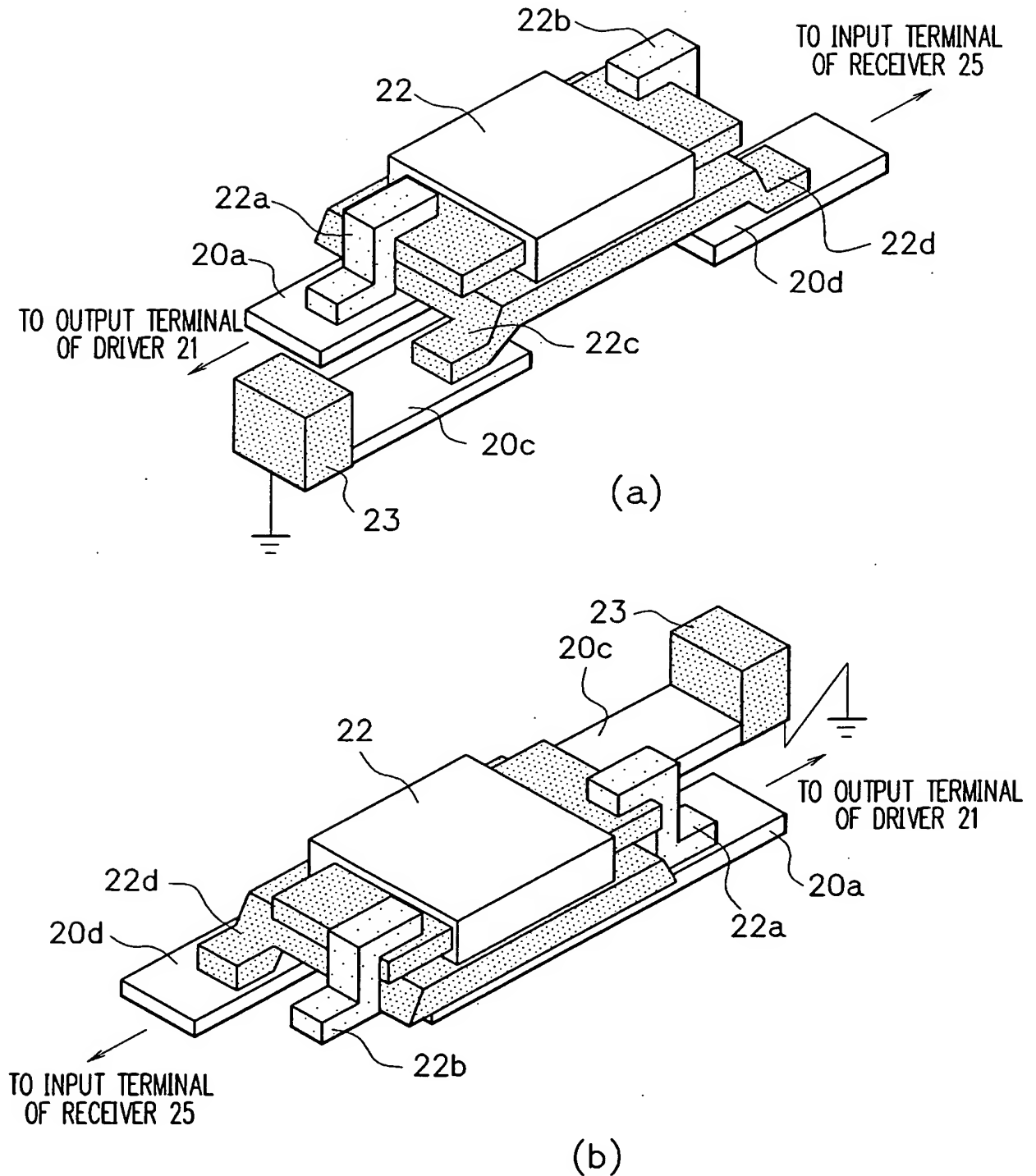


FIG. 17

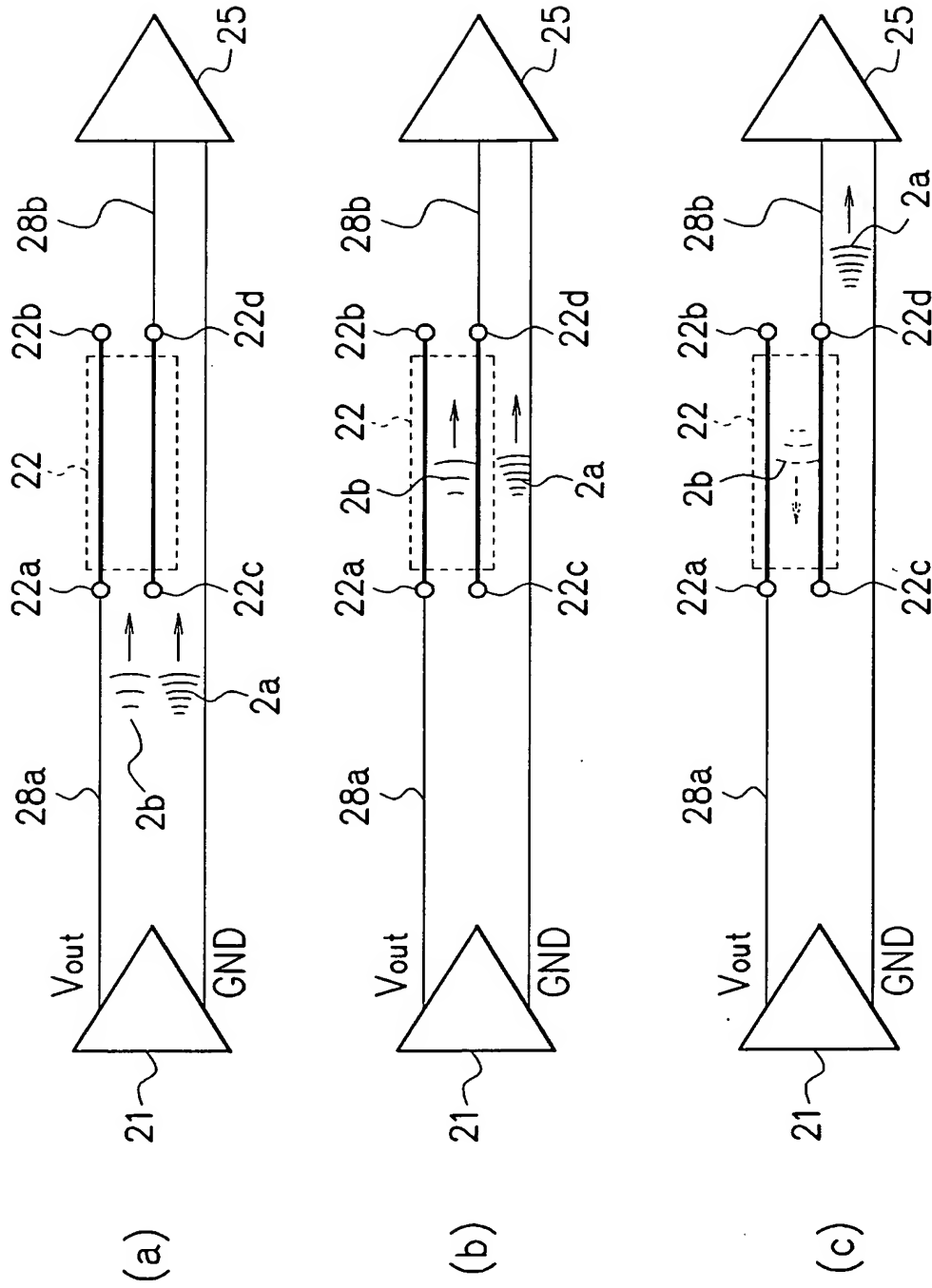


FIG. 18

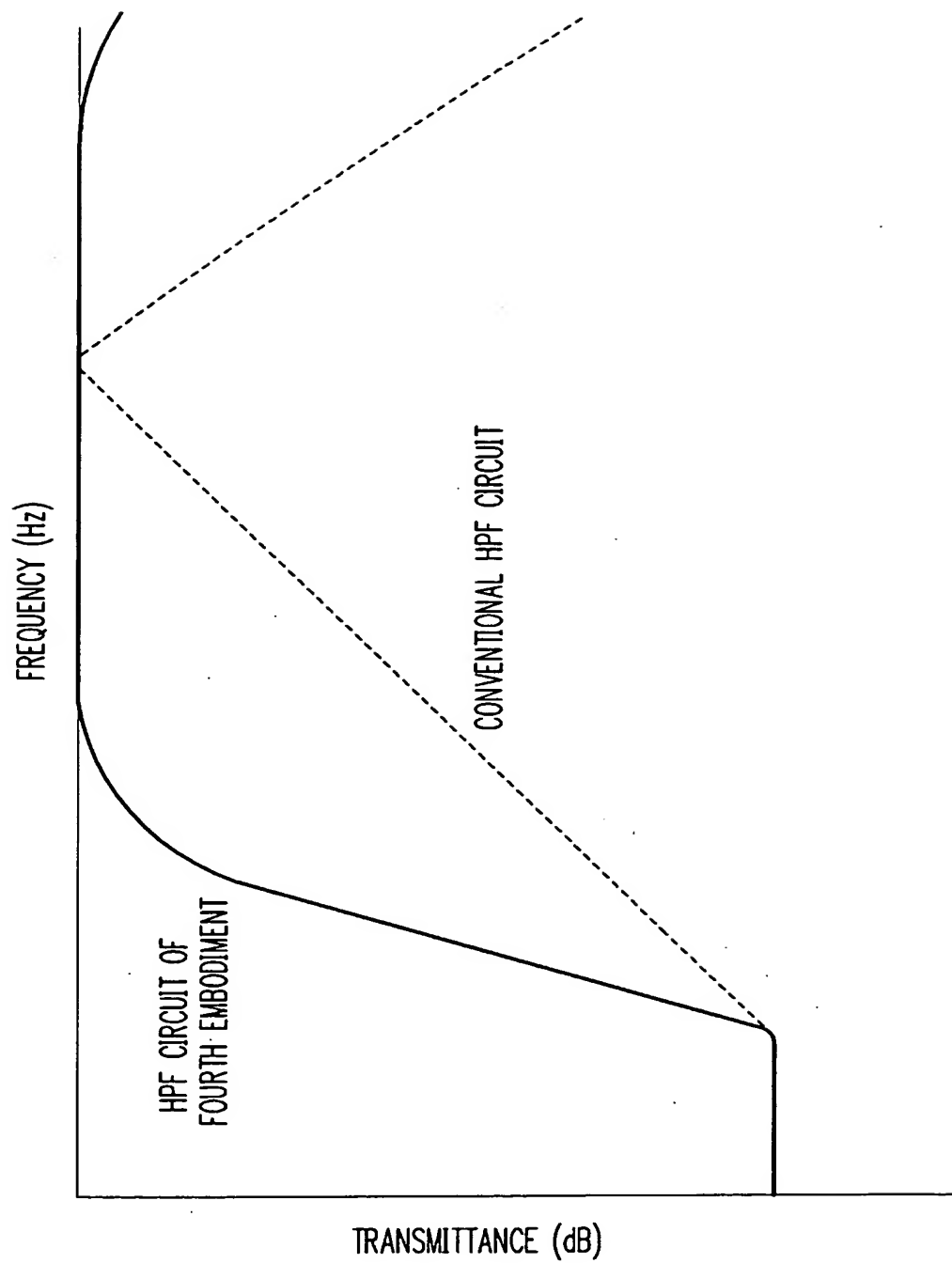
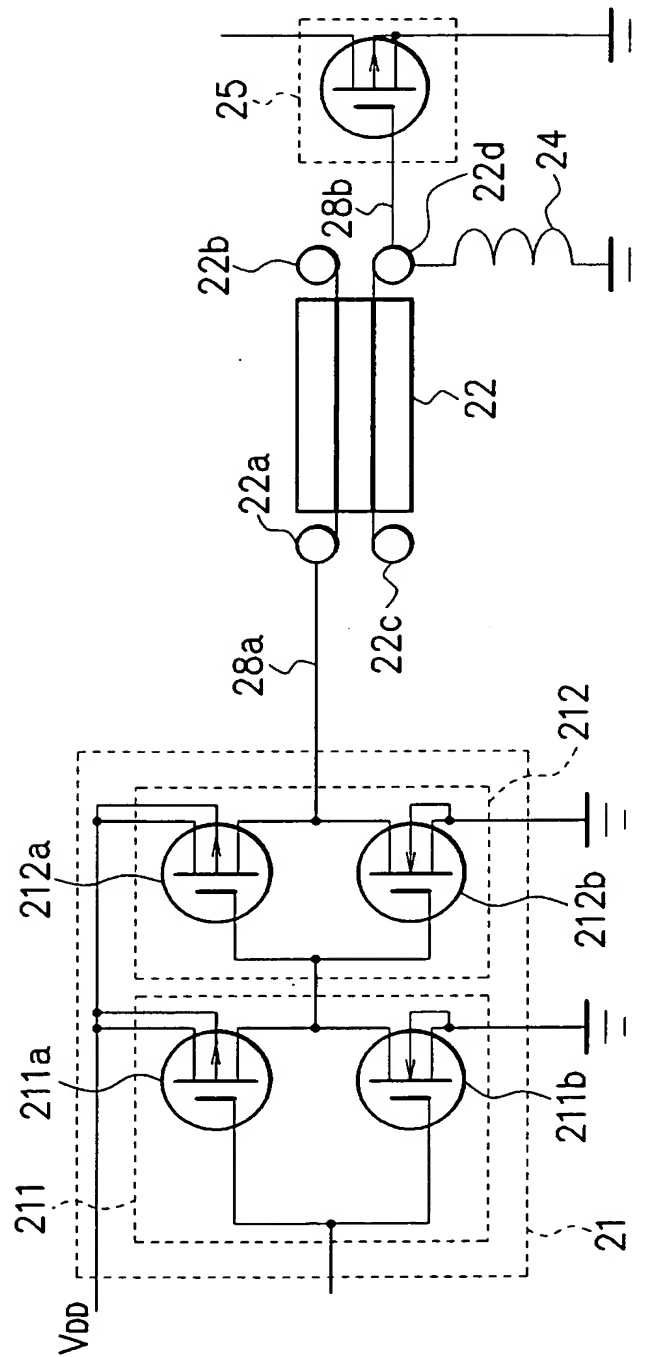


FIG. 19



F I G. 20

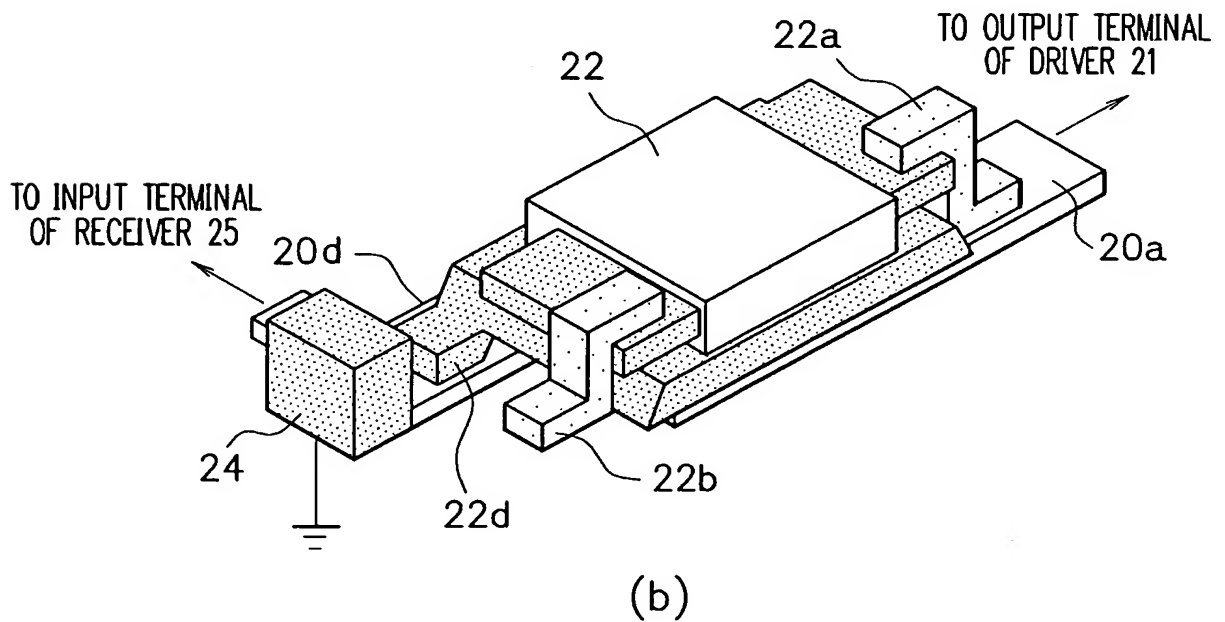
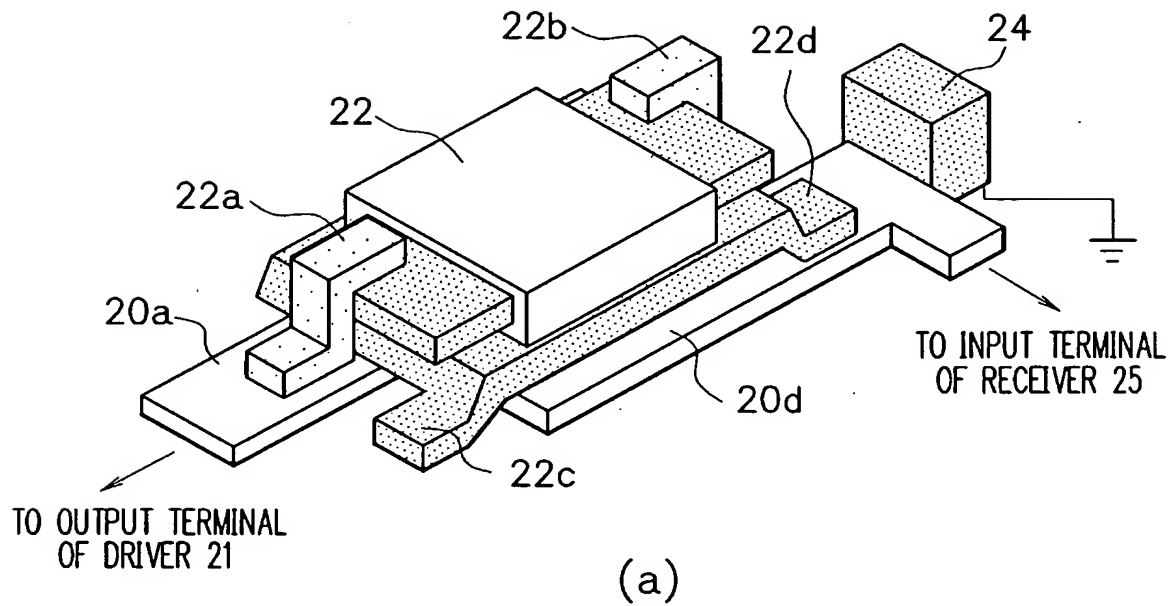


FIG. 22

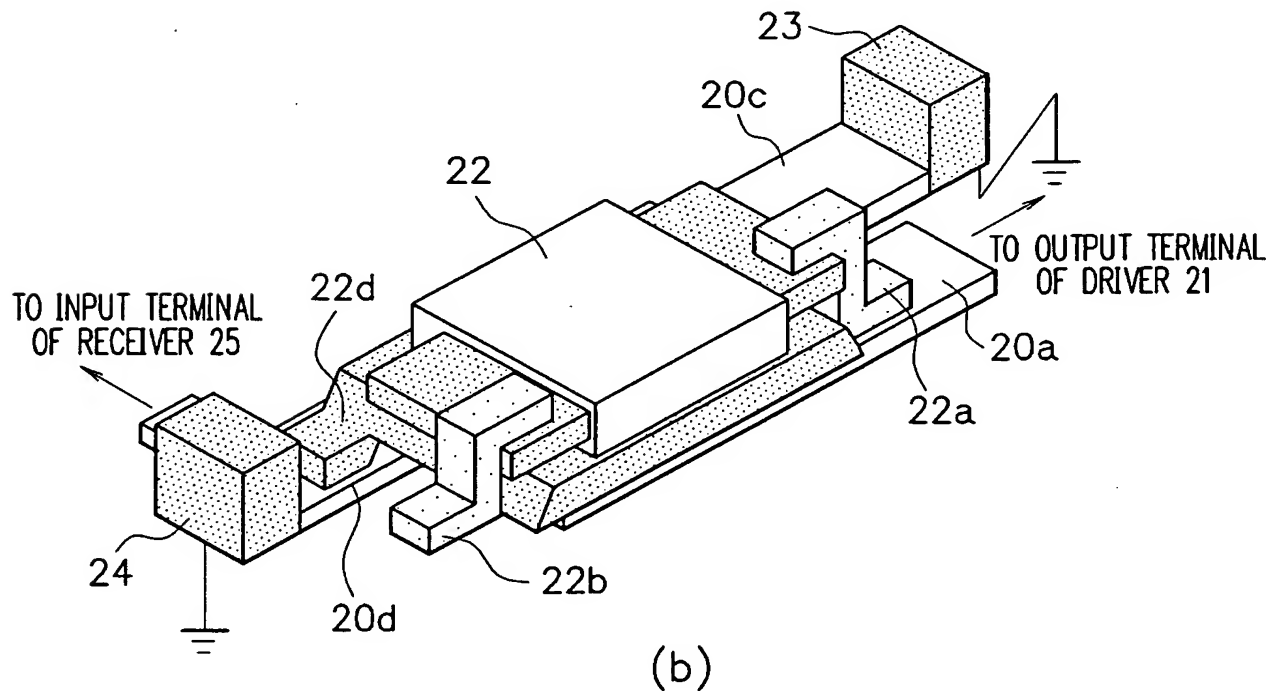
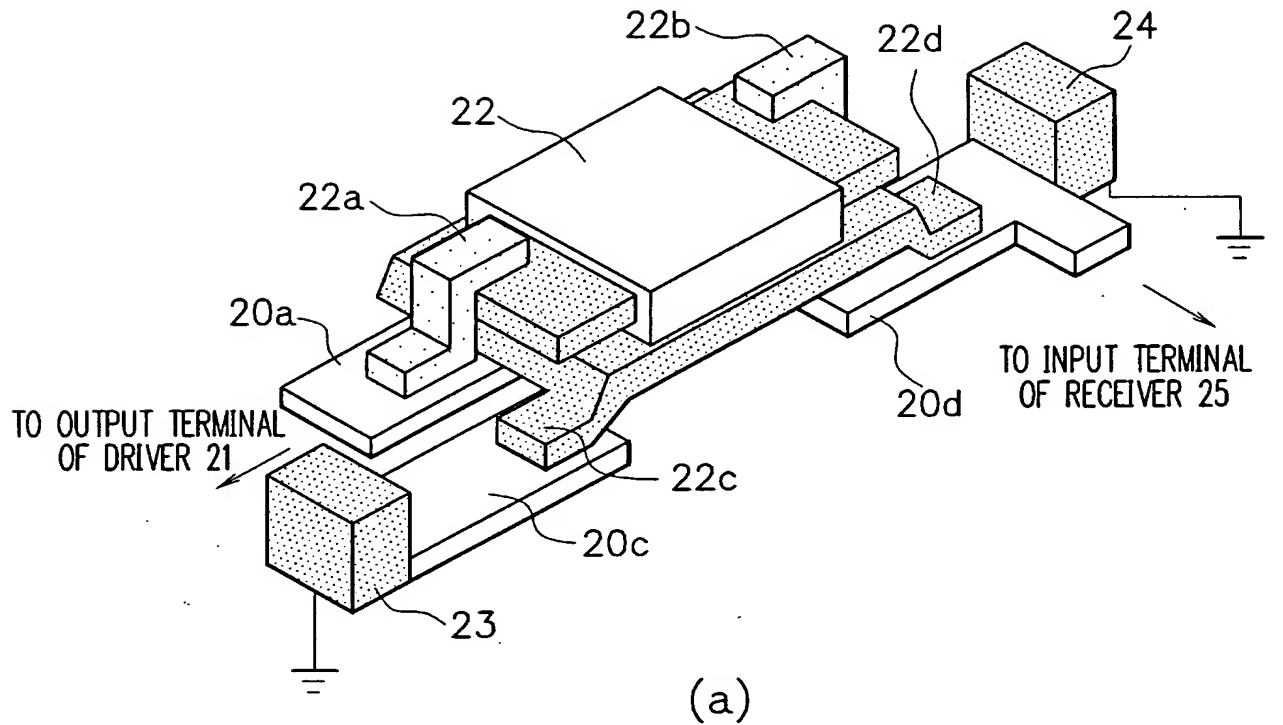


FIG. 23

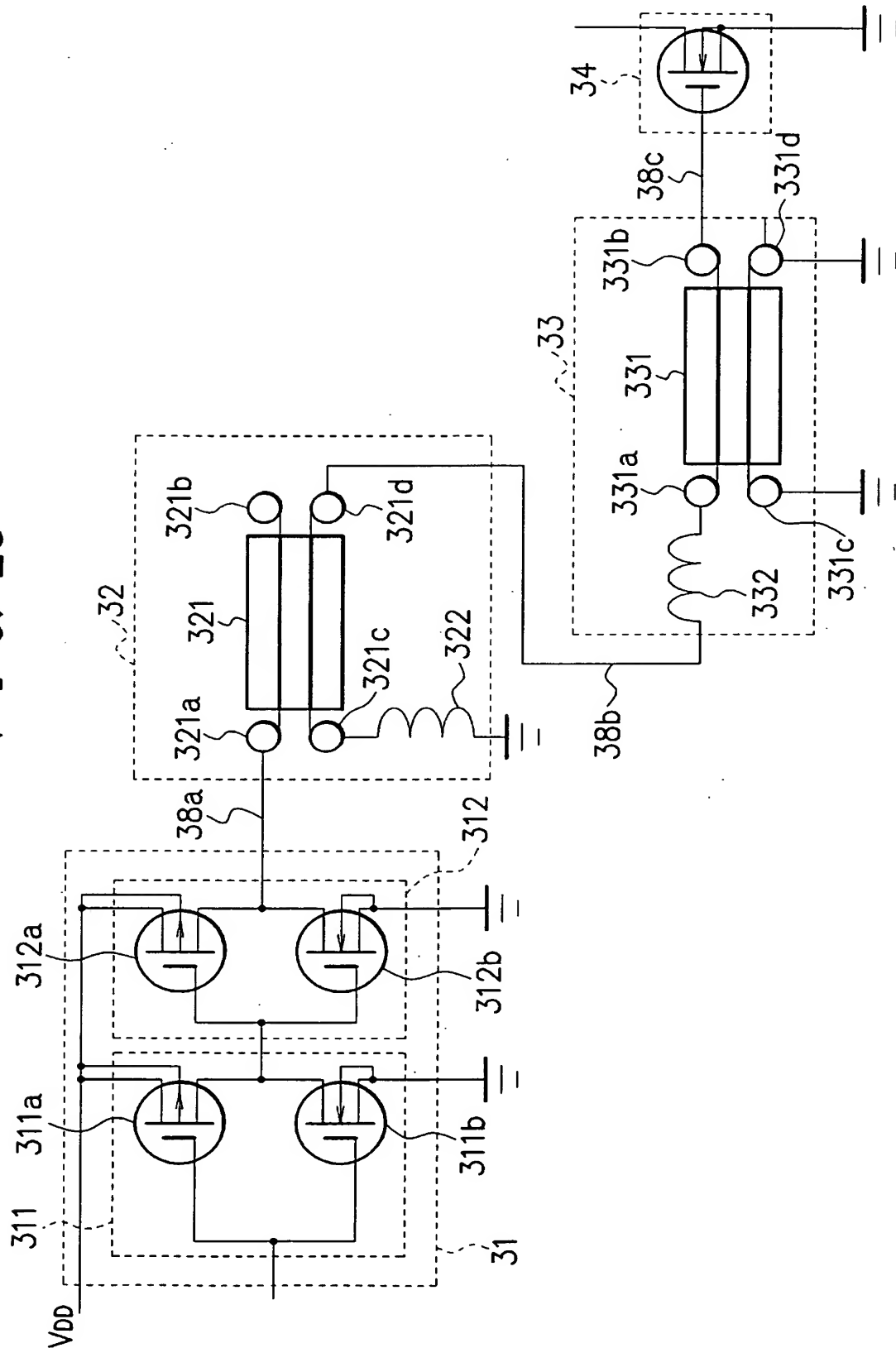


FIG. 24

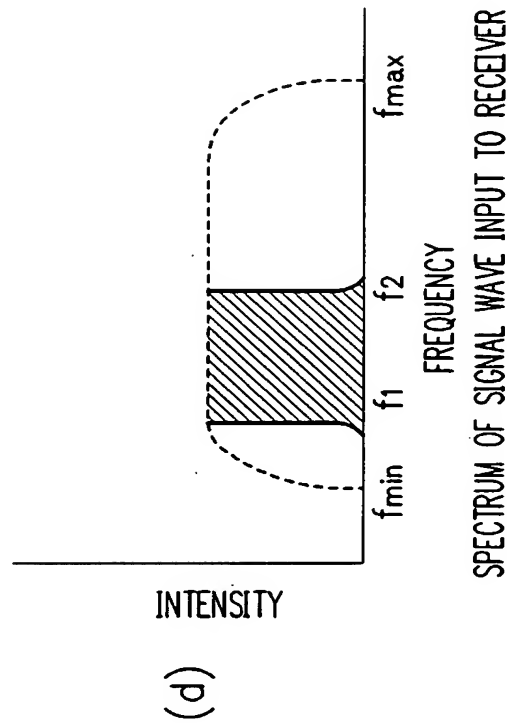
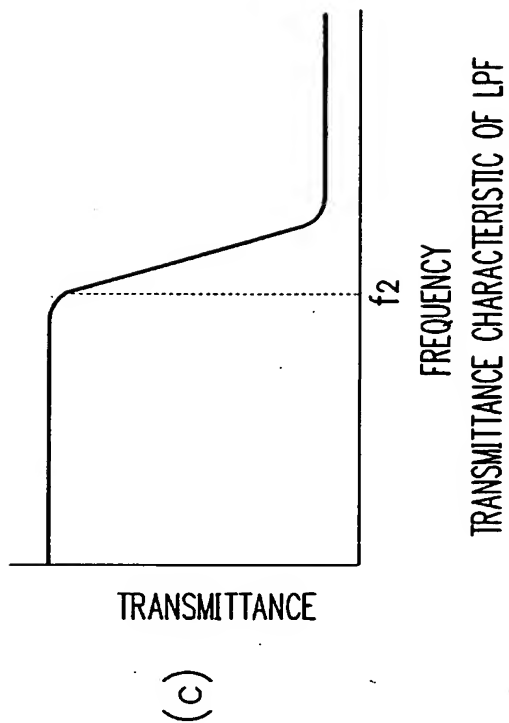
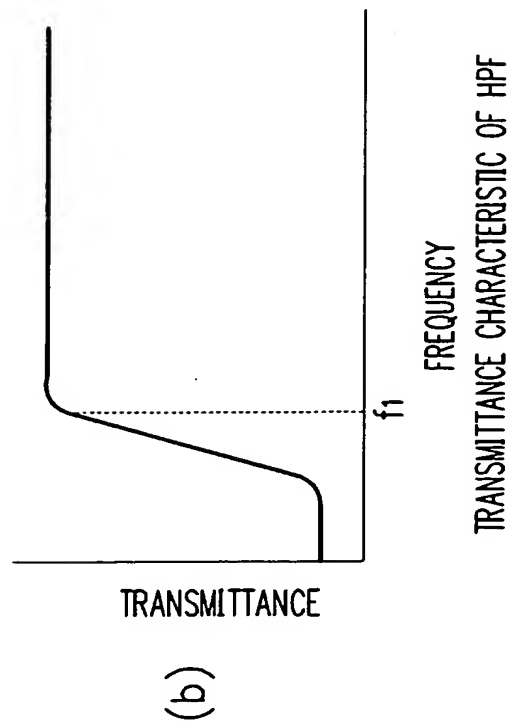
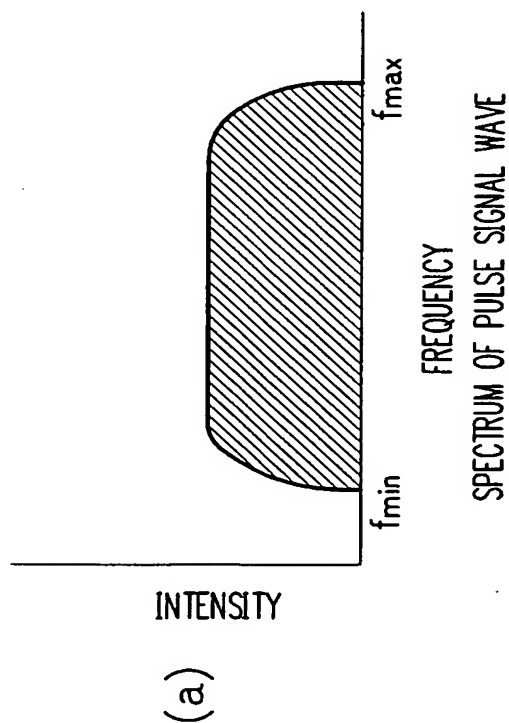


FIG. 25

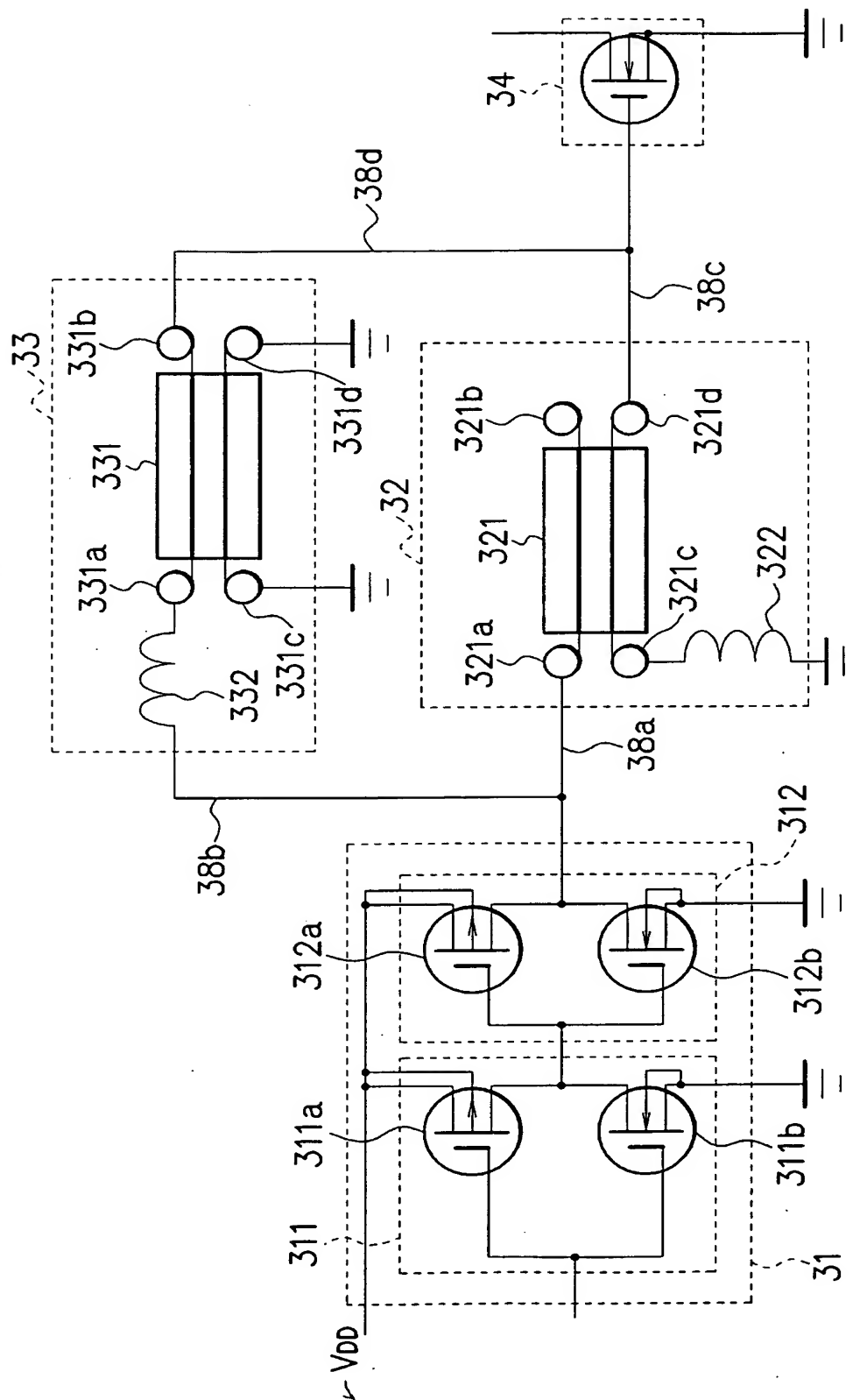


FIG. 26

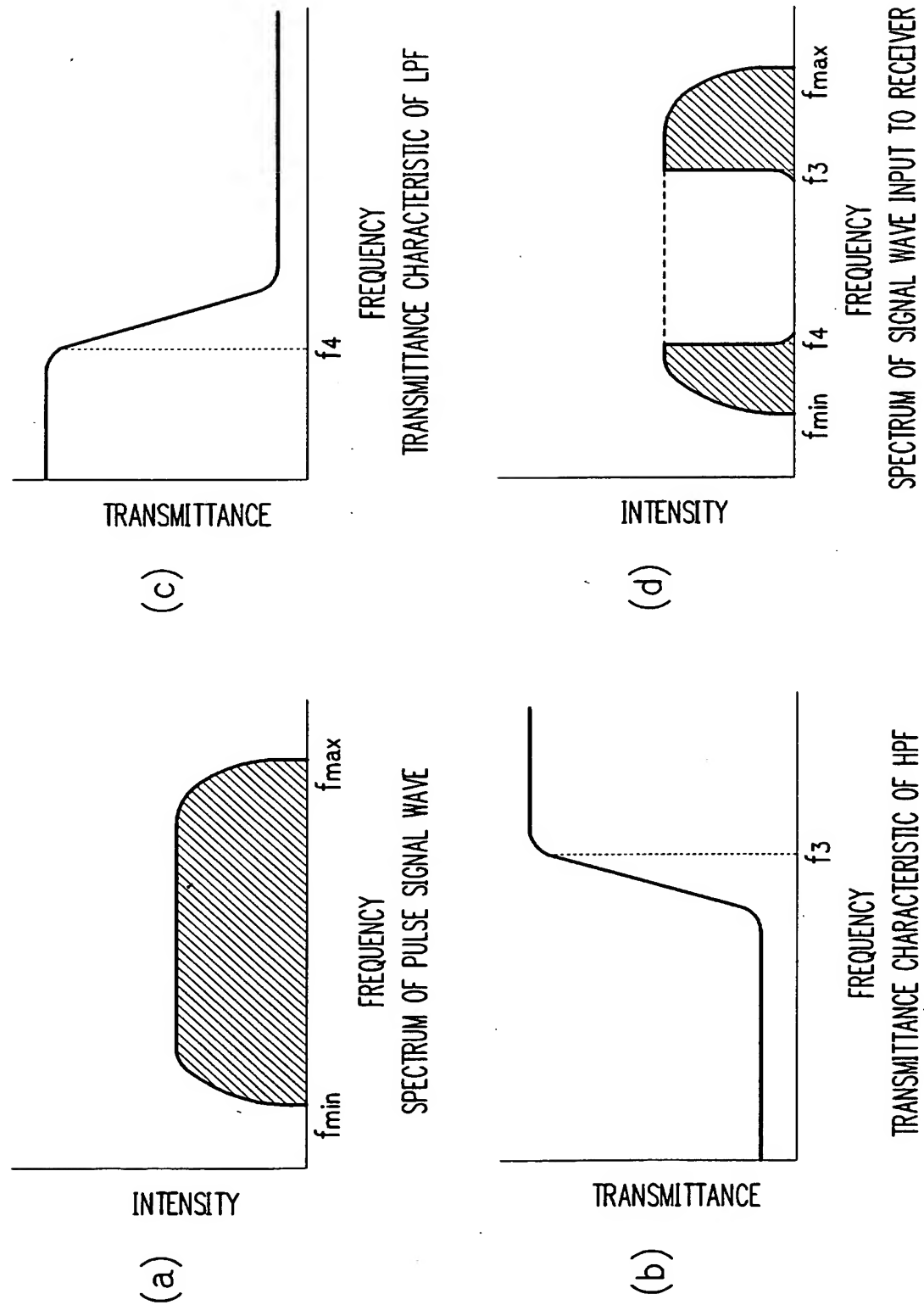
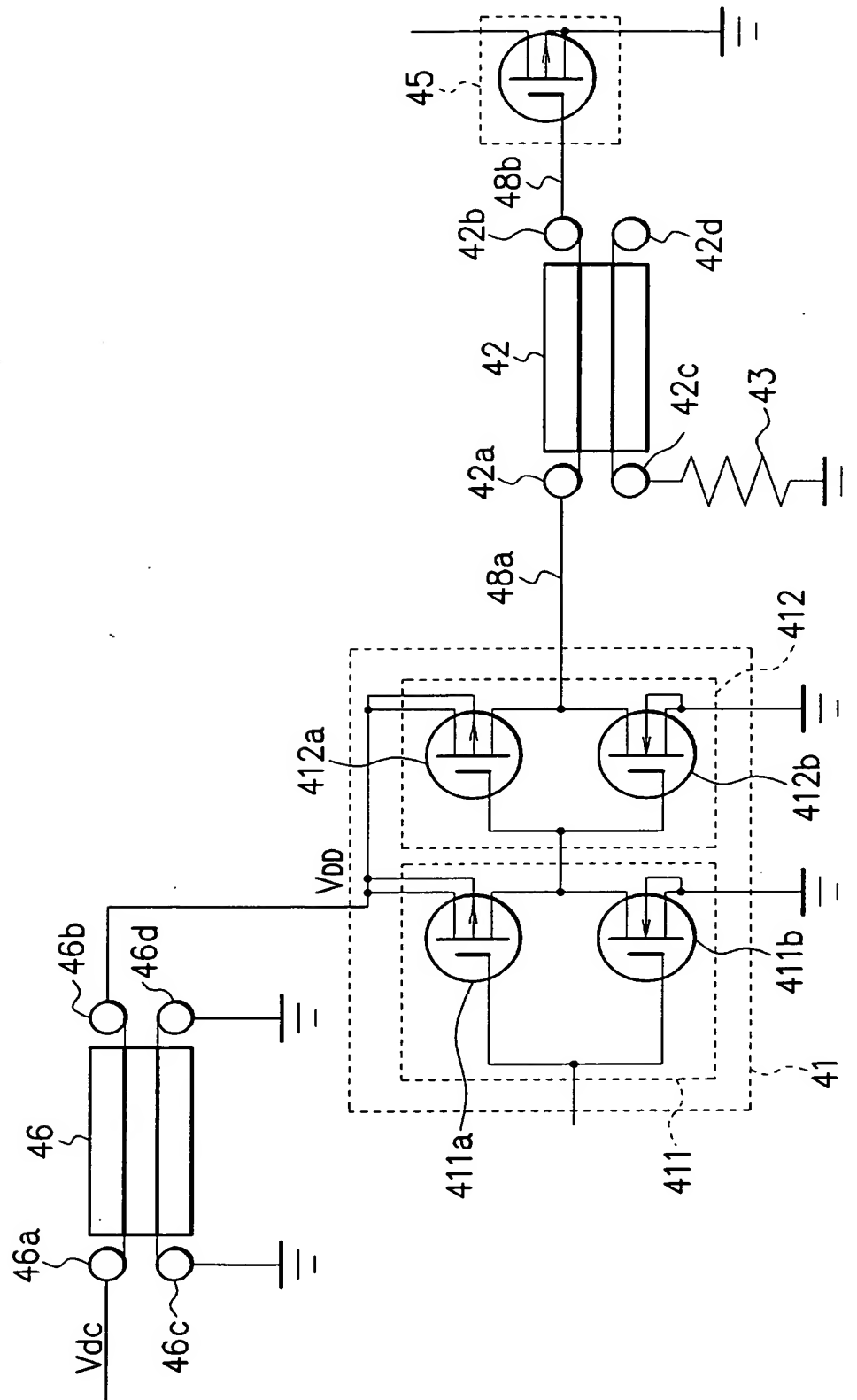


FIG. 27



F I G. 28

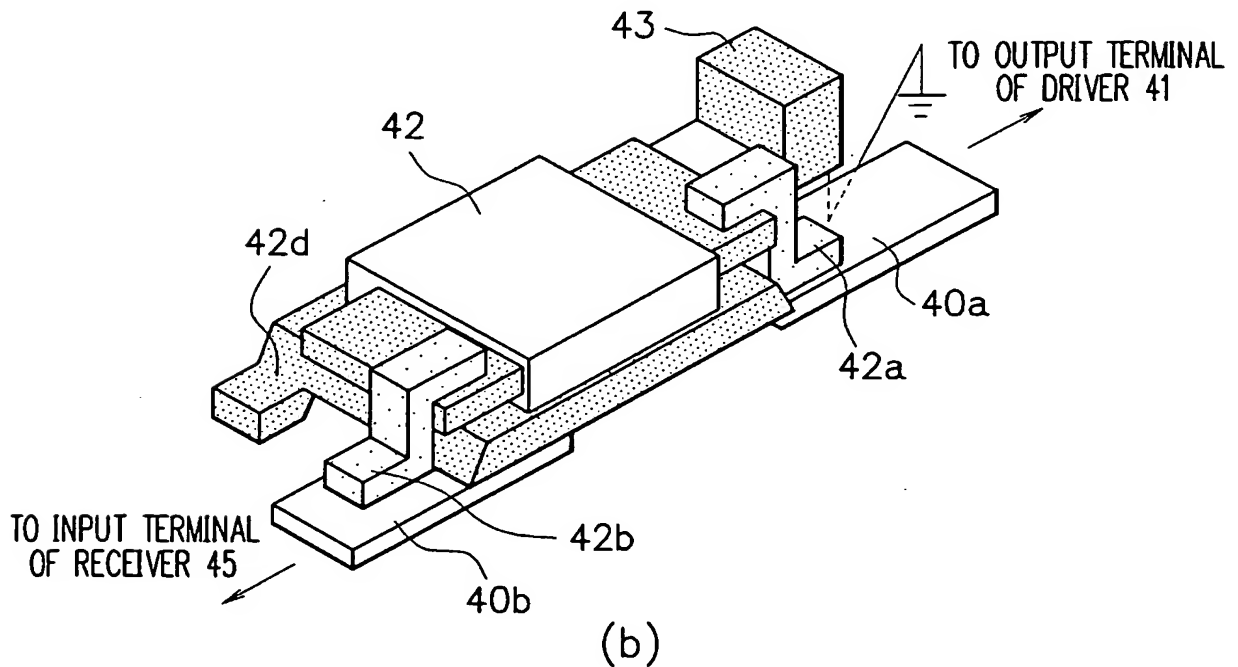
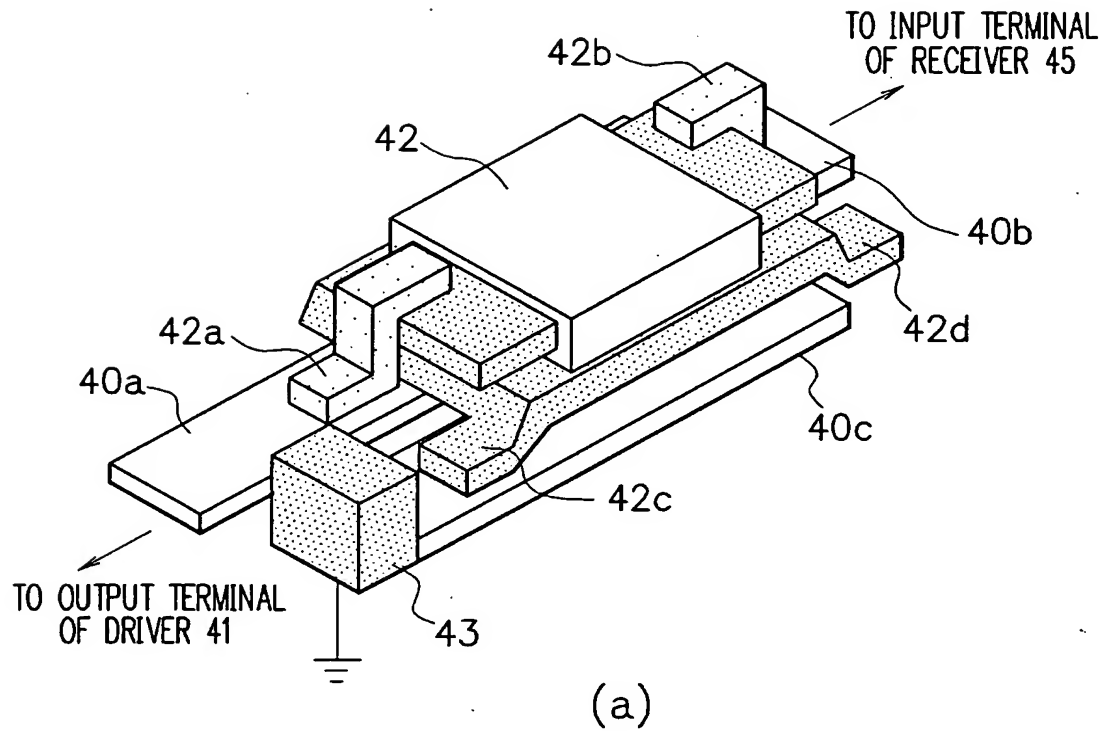


FIG. 29

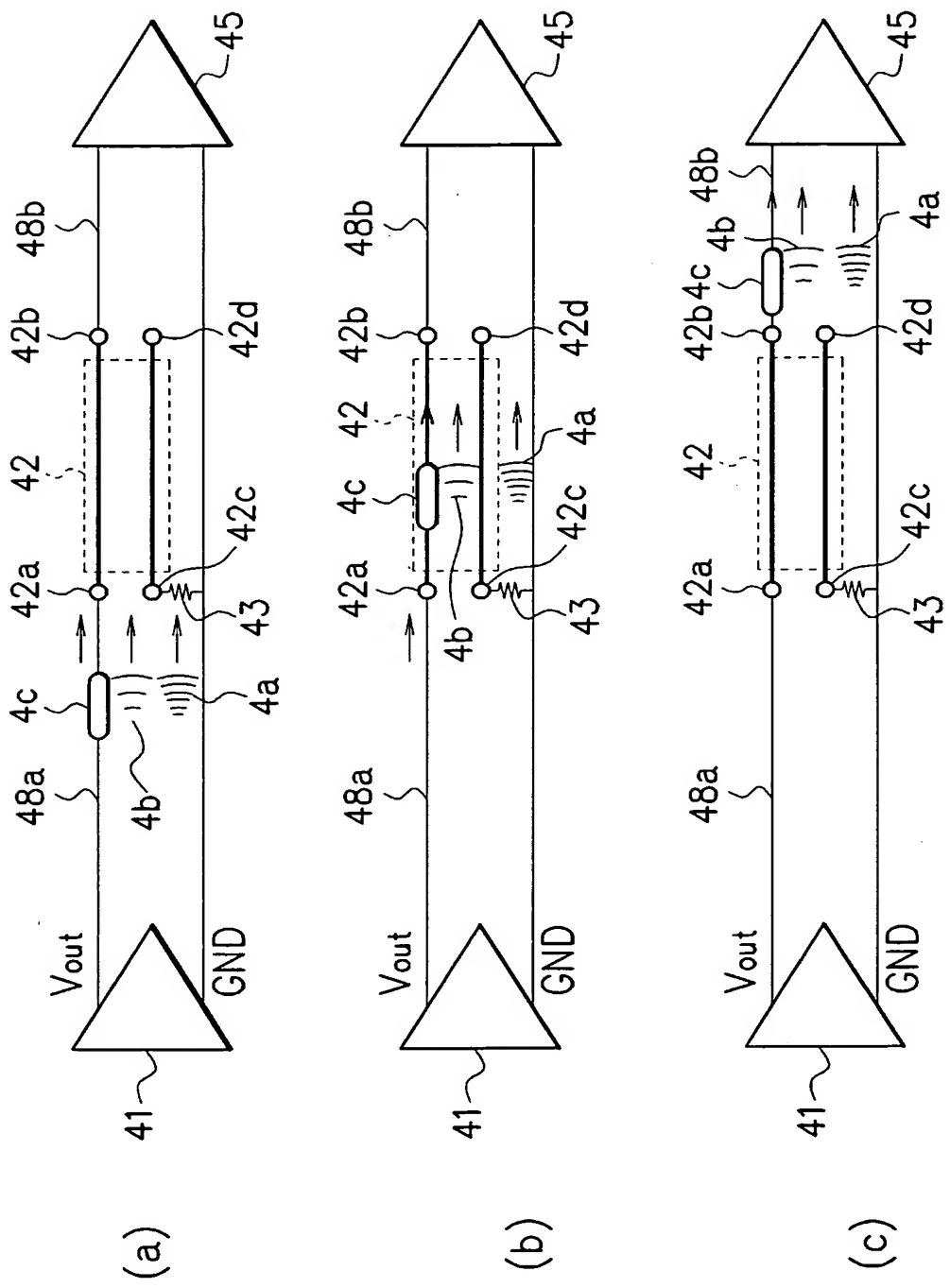
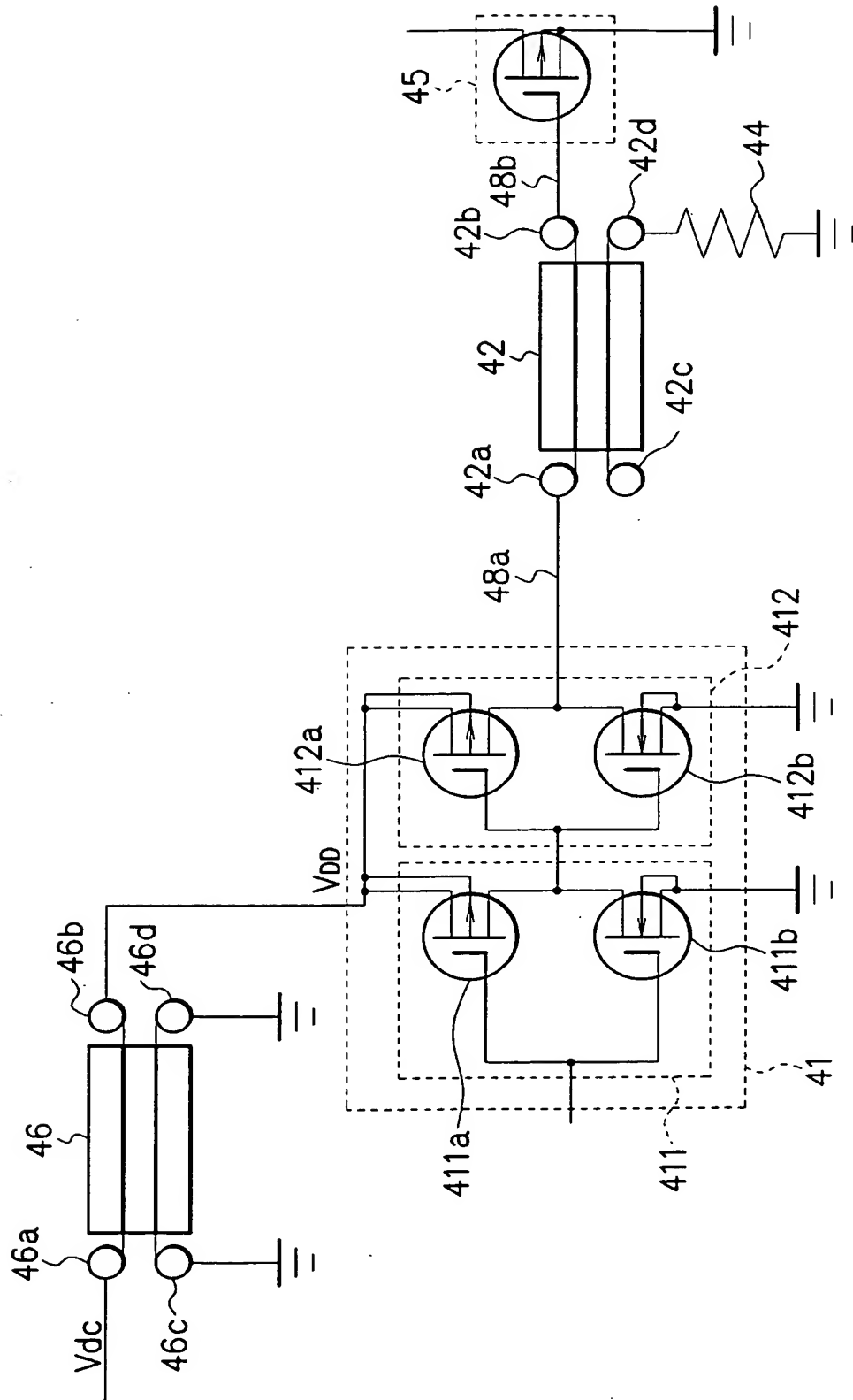
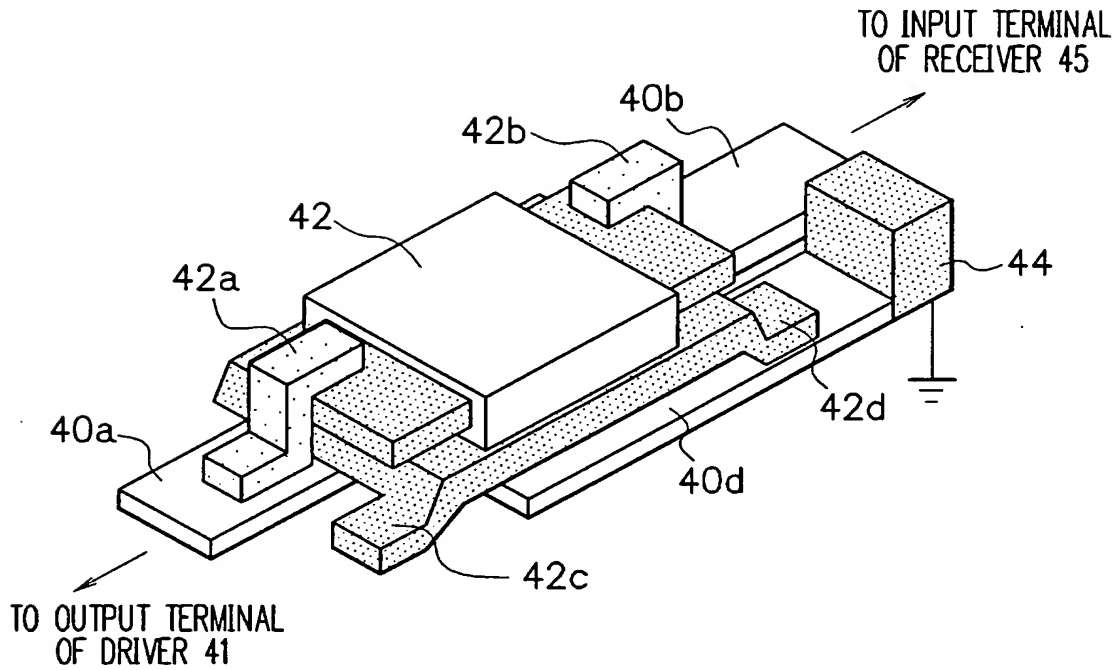


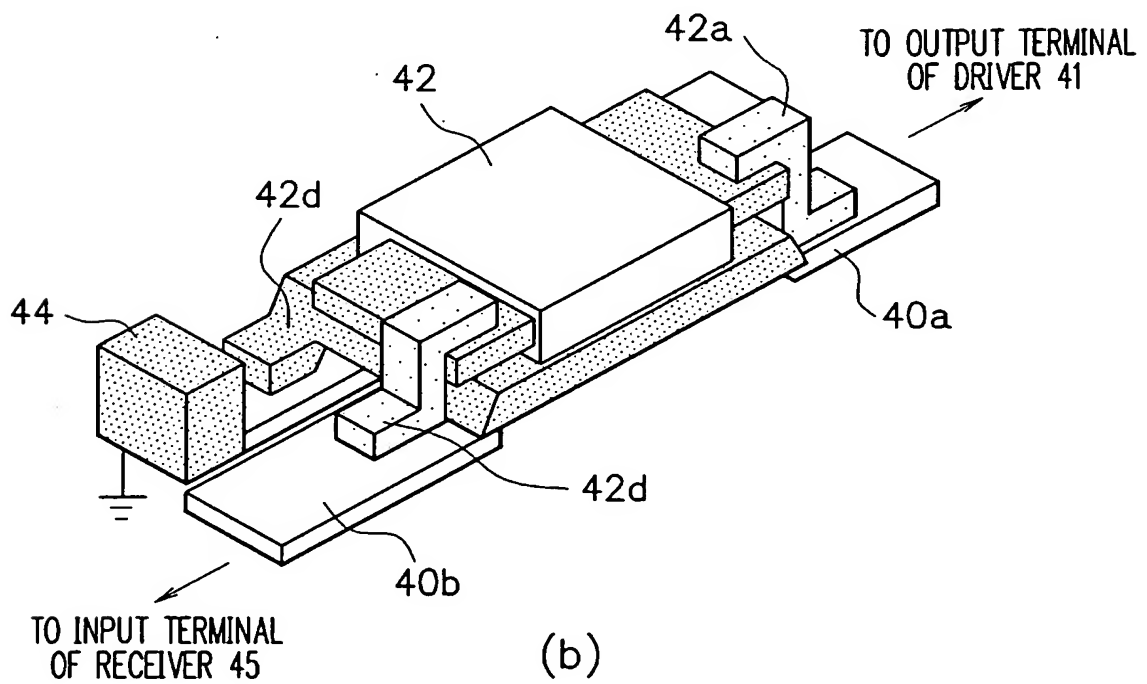
FIG. 30



F I G. 31



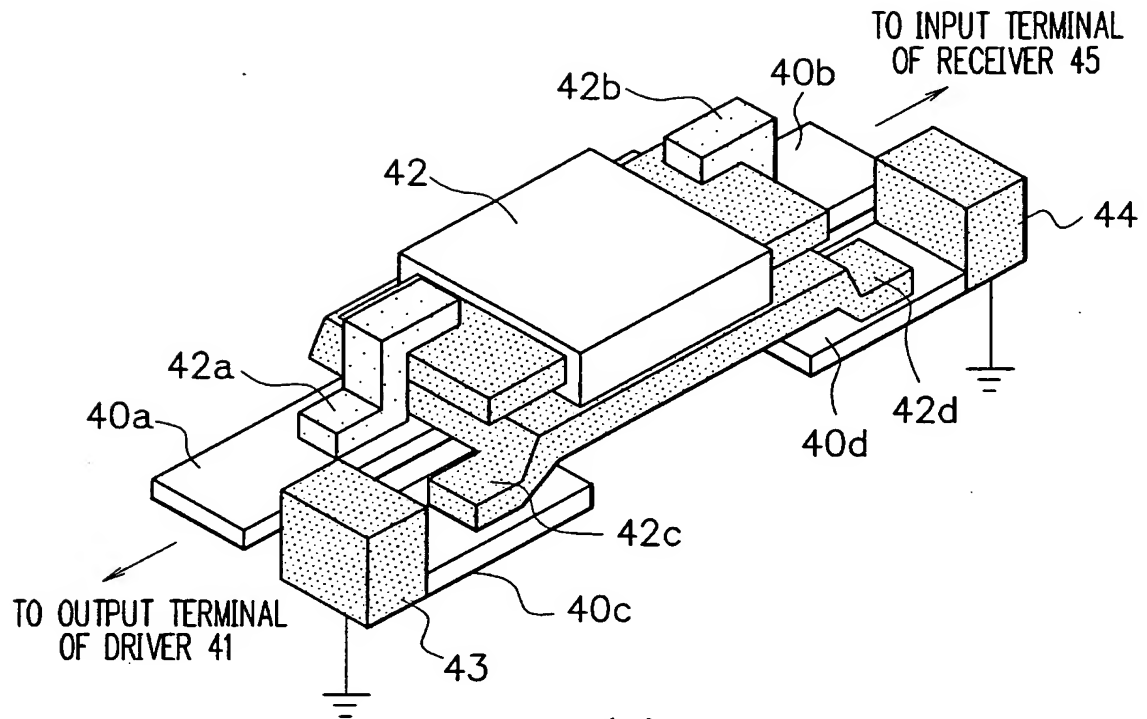
(a)



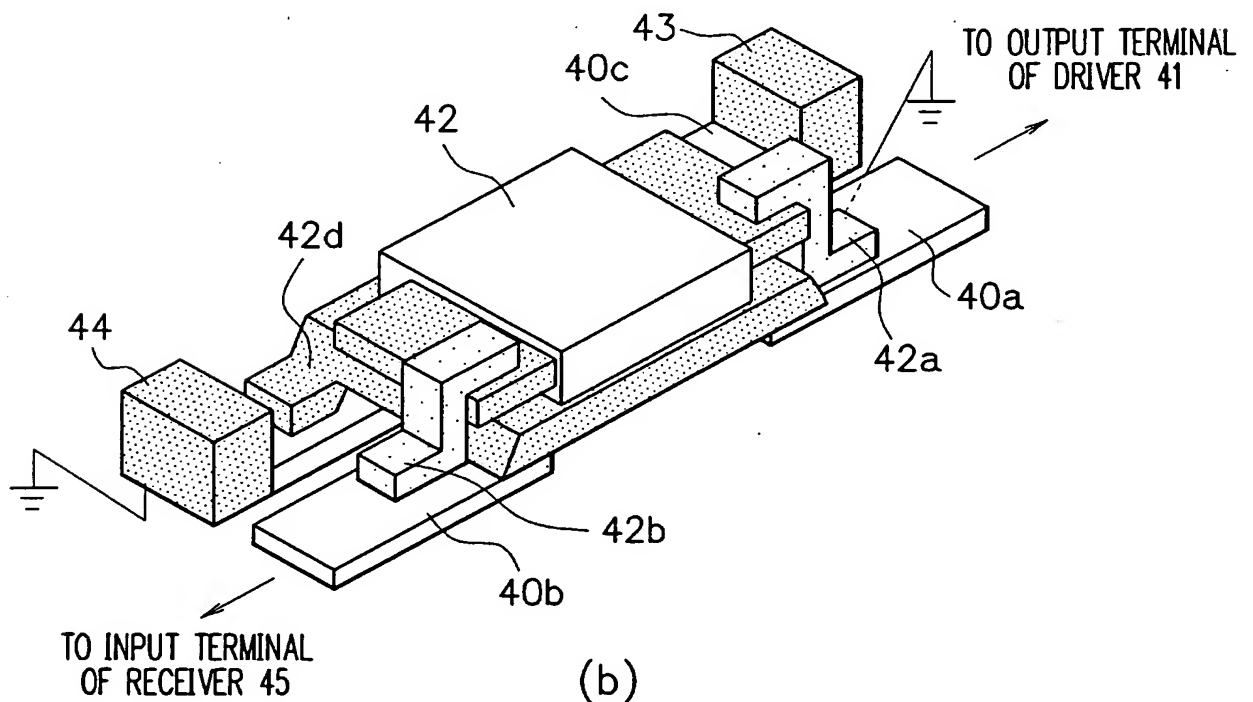
(b)

33/40

FIG. 33



(a)



(b)

FIG. 34

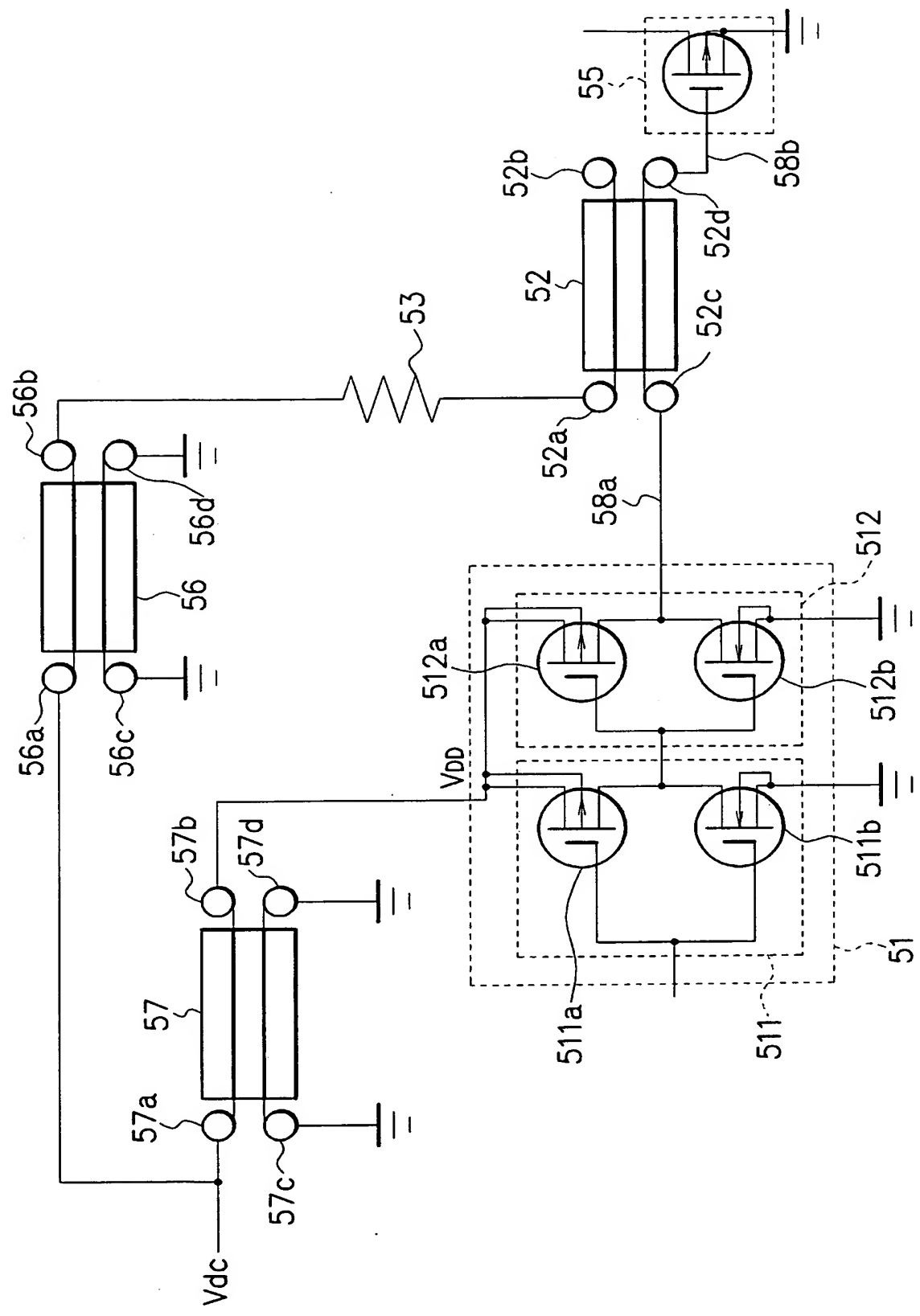


FIG. 35

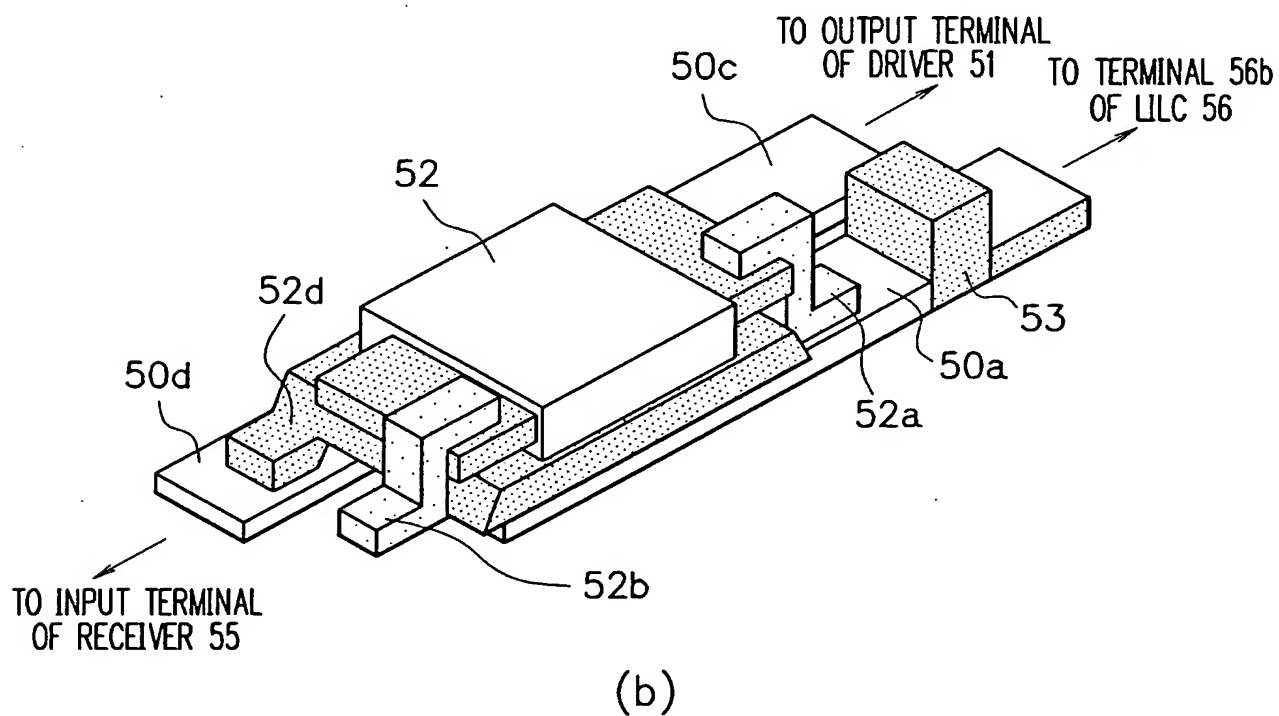
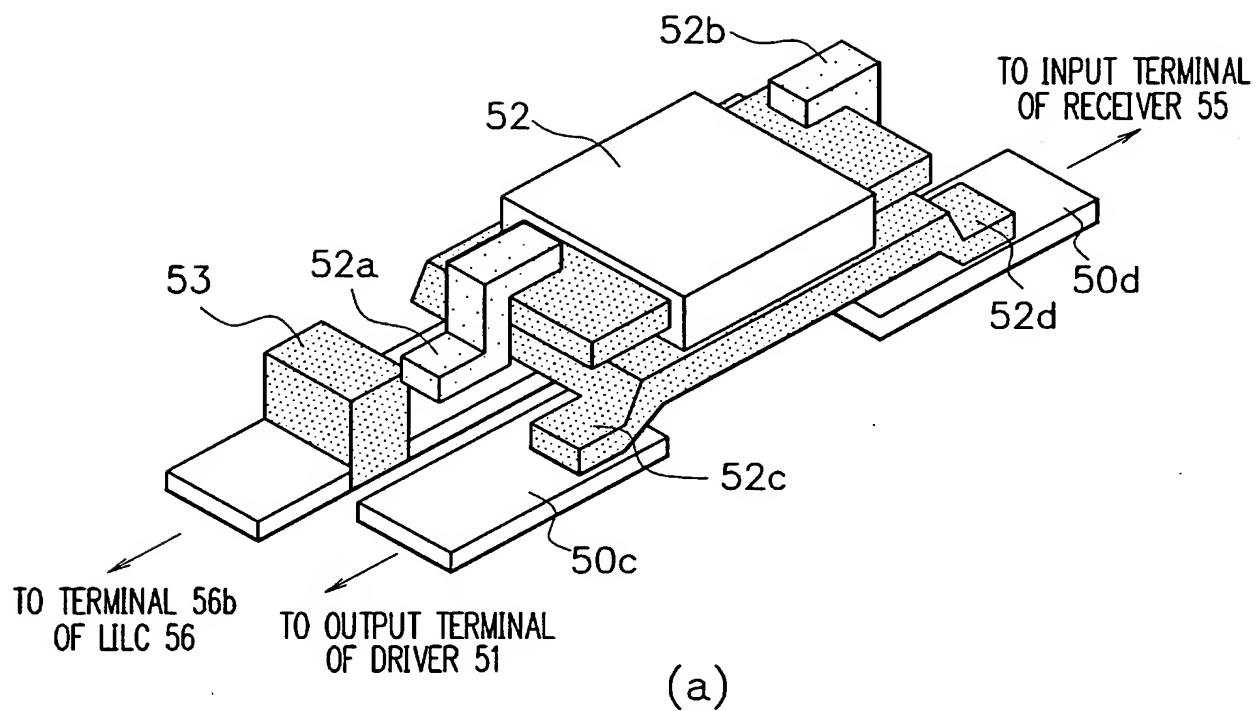
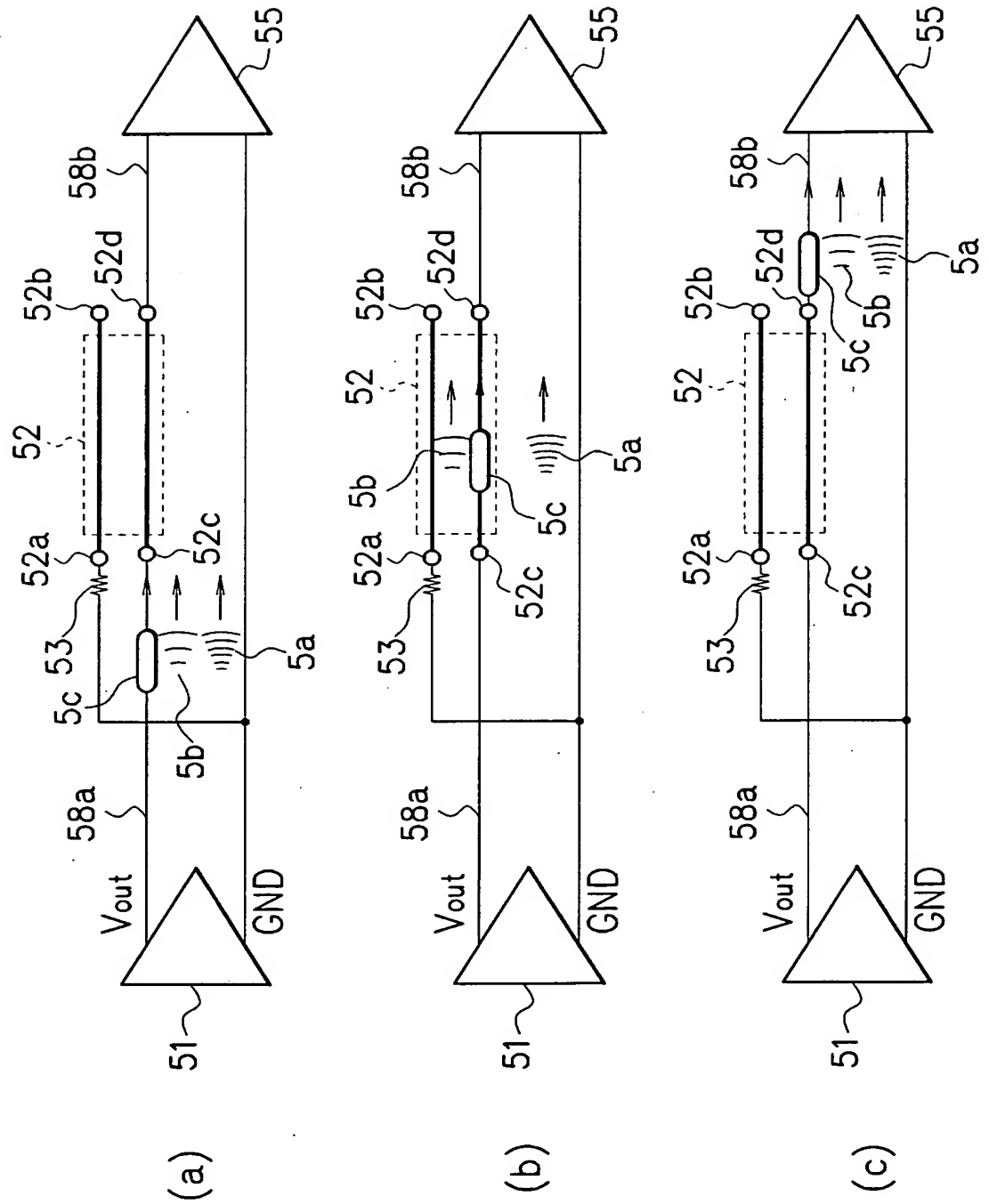


FIG. 36



[illegible]

FIG. 38

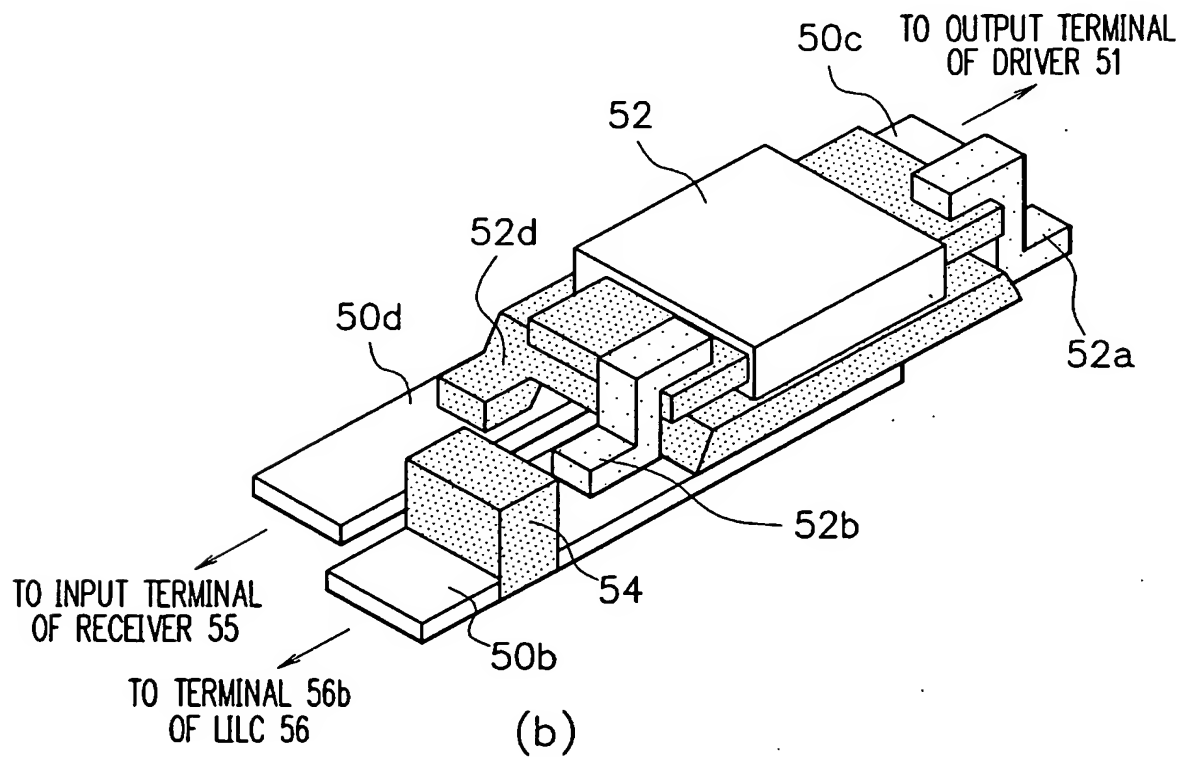
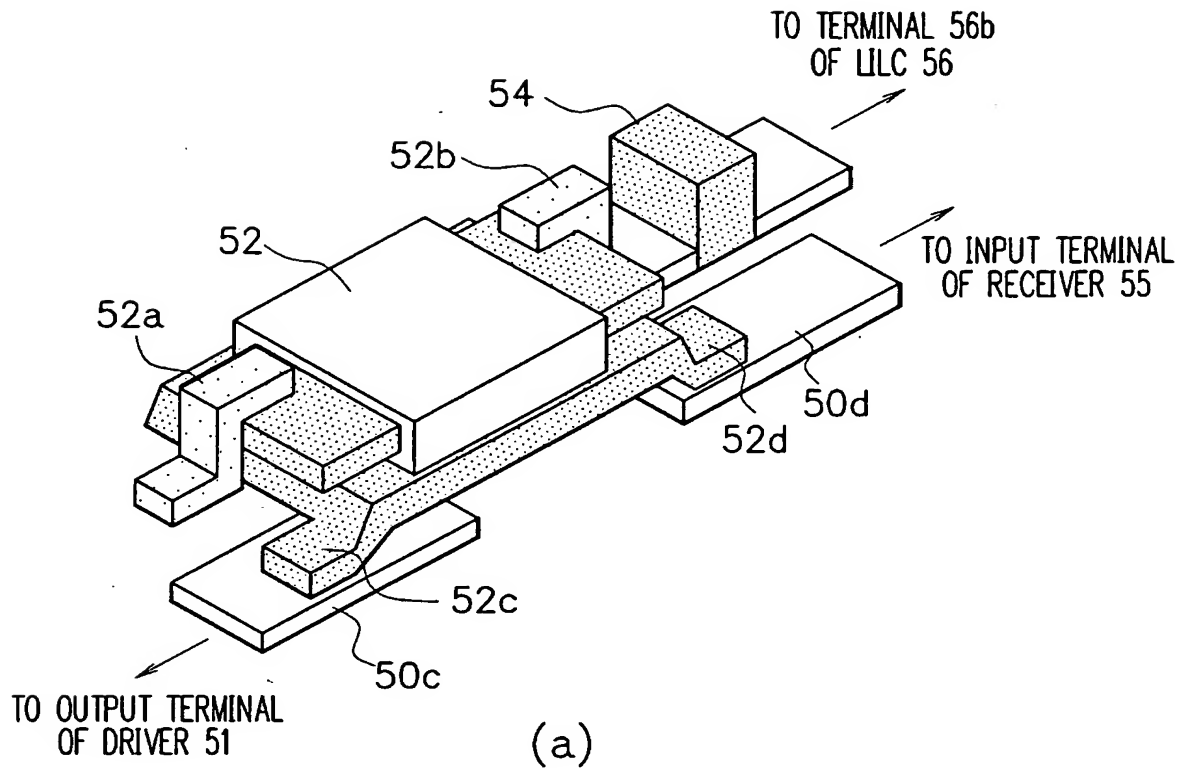


FIG. 39

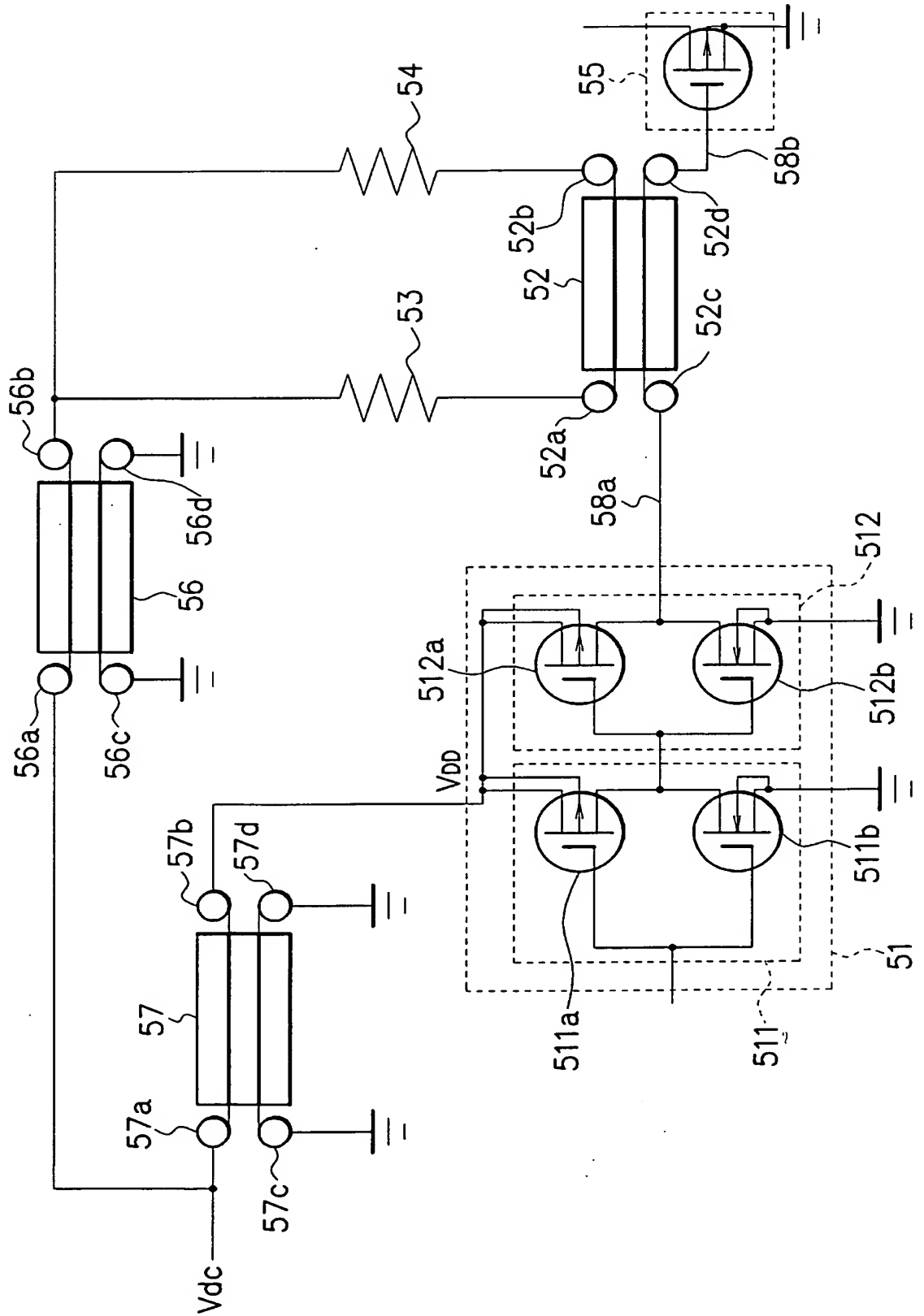


FIG. 40

